

Model Name: ST4265-BI

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCI EXPRESS*16 SLOT
10	PCH_FDI,DMI,USB,PCIE
11	PCH_DP,CLK BUFFER
12	PCH_HOST,SATA,PCI
13	PCH_GPIO,CTRL,AUDIO
14	PCH_PWR,GND
15	PCH_HDMI,DVI,DP
16	PCI EXPRESS*4 SLOT
17	PCI & PCIEX1 SLOT
18	LAN_ RTL8111DP
19	ITE 8728 LPC IO
20	COM,LPT
21	BIOS, TPM
22	VCORE /VAXG PWM_ISL95836-1
23	VCORE /VAXG PWM_ISL95836-2
24	RT8120-DDR POWWER
25	RT8120-VTT POWER
26	DISCRET POWER1
27	DISCRET POWER2

SHEET

TITLE

28	ATX,RUSB,PROCHOT-
29	AUDIO CODEC
30	REAR AUDIO JACK
31	FP,FUSB,SPKR
32	HWM,KB/MS, FAN CTRL
33	M9172-GSATA3
34	USB2.0 Hub uPD720114
35	
36	

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Title		
Cover Sheet		
Size	Document Number	Rev
Custom	ST4265-BI	1.2
Date:	Monday, December 03, 2012	Sheet 1 of 34

Model Name: ST4265-BI

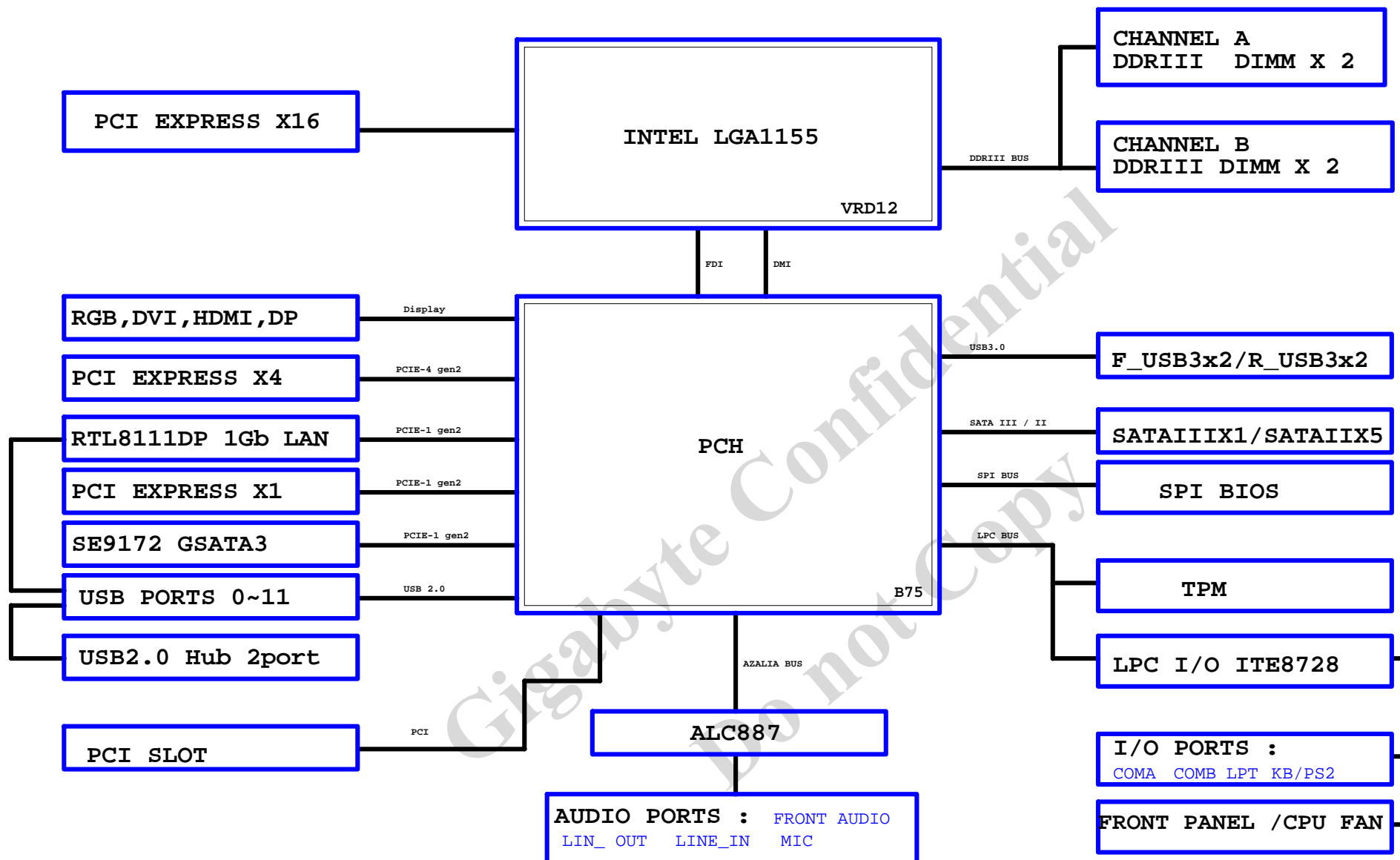
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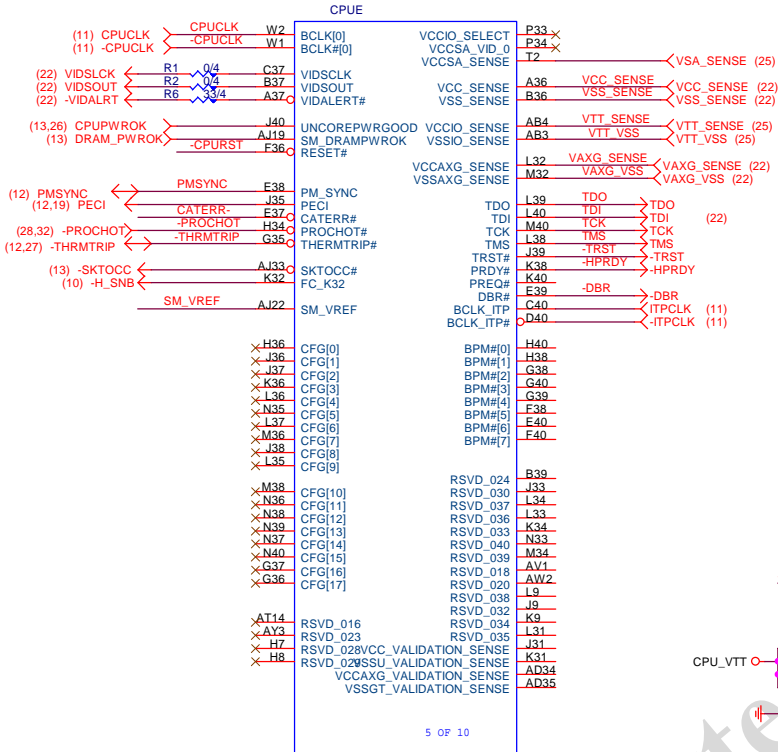
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Circuit or PCB layout change

[illegible]

BLOCK DIAGRAM



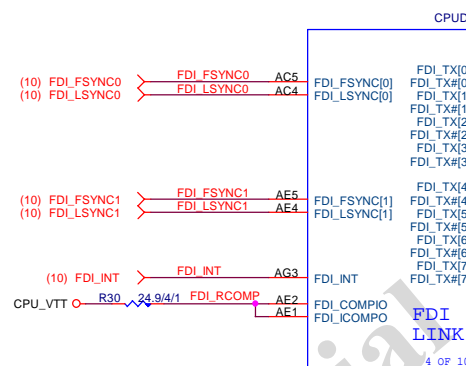
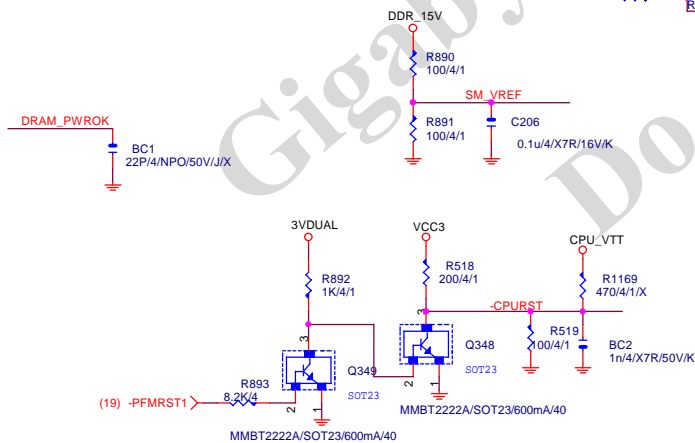


LGA1155[10SC1-F01155-21R_10SC1-F01155-22R]

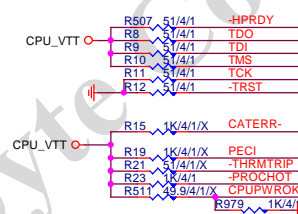
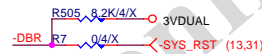
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0	RSVD	RSVD	RSVD
1	RSVD	RSVD	RSVD
2	NOR	Reverse	LANE REVERSAL[0..x16]
3	RSVD	RSVD	RSVD
4	RSVD	RSVD	RSVD
7	RSVD	RSVD	RSVD
8	RSVD	RSVD	RSVD
9	RSVD	RSVD	RSVD
10	RSVD	RSVD	RSVD
11	RSVD	RSVD	RSVD
12	RSVD	RSVD	RSVD
13	RSVD	RSVD	RSVD
14	RSVD	RSVD	RSVD
15	RSVD	RSVD	RSVD
16	RSVD	RSVD	RSVD
17	RSVD	RSVD	RSVD

CFG6	CFG5	PCIE CONFIG
1	1	1x16, Default
1	0	2x8
0	1	RSVD
0	0	8x4, 4x4

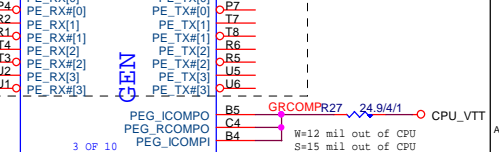
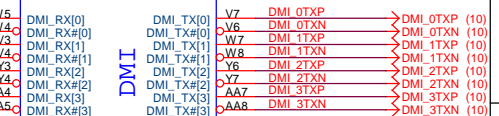
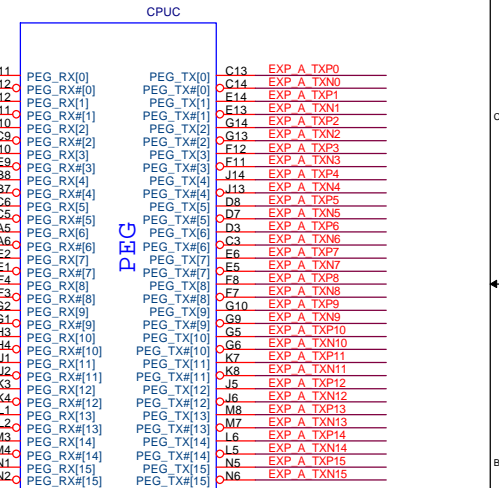
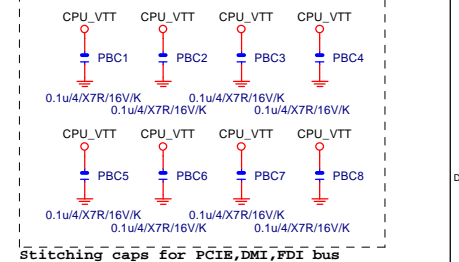
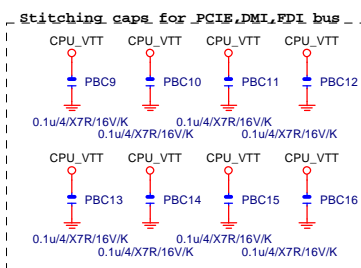
CFG 0-17 all internal PULL-UP



LGA1155[10SC1-F01155-21R_10SC1-F01155-22R]



-Prochot pu change to 1K from 51, for the PWM IC sink level must under 0.3VTT when drive Low.



LGA1155[10SC1-F01155-21R_10SC1-F01155-22R]		
Title		
CPU LGA1155-A		
Size	Document Number	Rev
Custpm	ST4265-BI	1.2
Date:	Monday, December 03, 2012	Sheet 4 of 34

CPUA

MAAA0	AV27	SA_MAI[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MAI[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MAI[2]			
MAAA3	AW23	SA_MAI[3]			
MAAA4	AV23	SA_MAI[4]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MAI[5]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MAI[6]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MAI[7]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MAI[8]	SA_DQ[4]	AJ2	MDA4
MAAA9	AT22	SA_MAI[9]	SA_DQ[5]	AJ1	MDA5
MAAA10	AV28	SA_MAI[10]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MAI[11]	SA_DQ[7]	AL1	MDA7
MAAA12	AT21	SA_MAI[12]			
MAAA13	AW32	SA_MAI[13]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MAI[14]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MAI[15]			
(7) -SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
(7) -SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
(7) -SRASA	AU28	SA_RAS#	SA_DQ[10]	AR3	MDA10
(7) SBAA0	AY29	SA_BS[0]	SA_DQ[11]	AR4	MDA12
(7) SBAA1	AW28	SA_BS[1]	SA_DQ[12]	AN2	MDA11
(7) SBAA2	AV20	SA_BS[2]	SA_DQ[13]	AN3	MDA13
			SA_DQ[14]	AR2	MDA14
			SA_DQ[15]	AR1	MDA15
(7) -CSA0	AU29	SA_CS#	SA_DQS[2]	AW4	DQSA2
(7) -CSA1	AV32	SA_CS#	SA_DQS[2]	AW4	-DQSA2
(7) -CSA2	AW30	SA_CS#			
(7) -CSA3	AU33	SA_CS#			
(7) CKEA0	AV19	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
(7) CKEA1	AT19	SA_CKE[1]	SA_DQ[17]	AW3	MDA17
(7) CKEA2	AU18	SA_CKE[2]	SA_DQ[18]	AV5	MDA18
(7) CKEA3	AV18	SA_CKE[3]	SA_DQ[19]	AW5	MDA19
			SA_DQ[20]	AU2	MDA20
MODT_A0	AV31	SA_ODT[0]	SA_DQ[21]	AU3	MDA21
MODT_A1	AU32	SA_ODT[1]	SA_DQ[22]	AU5	MDA22
MODT_A2	AU30	SA_ODT[2]	SA_DQ[23]	AY5	MDA23
MODT_A3	AW33	SA_ODT[3]			
(7) DCLKA0	AY25	SA_CLK[0]	SA_DQS[3]	AV8	DQSA3
(7) -DCLKA0	AW25	SA_CLK[0]	SA_DQS[3]	AW8	-DQSA3
(7) DCLKA1	AU24	SA_CLK[1]			
(7) -DCLKA1	AU25	SA_CLK[1]	SA_DQ[24]	AY7	MDA24
(7) DCLKA2	AW27	SA_CLK[2]	SA_DQ[25]	AU7	MDA25
(7) -DCLKA2	AY27	SA_CLK[2]	SA_DQ[26]	AV9	MDA26
(7) DCLKA3	AW26	SA_CLK[3]	SA_DQ[27]	AU9	MDA27
(7) -DCLKA3	AV26	SA_CLK[3]	SA_DQ[28]	AV7	MDA28
			SA_DQ[29]	AW7	MDA29
			SA_DQ[30]	AW9	MDA30
			SA_DQ[31]	AY9	MDA31
			SA_DQS[4]	AV37	DQSA4
			SA_DQS[4]	AV36	-DQSA4
			SA_DQ[32]	AU35	MDA32
			SA_DQ[33]	AW37	MDA33
			SA_DQ[34]	AU39	MDA34
			SA_DQ[35]	AU36	MDA35
			SA_DQ[36]	AW35	MDA36
			SA_DQ[37]	AY36	MDA37
			SA_DQ[38]	AU38	MDA38
			SA_DQ[39]	AU37	MDA39
			SA_DQS[5]	AP38	DQSA5
			SA_DQS[5]	AP39	-DQSA5
			SA_DQ[40]	AR40	MDA40
			SA_DQ[41]	AR37	MDA41
			SA_DQ[42]	AN38	MDA42
			SA_DQ[43]	AN37	MDA43
			SA_DQ[44]	AR39	MDA44
			SA_DQ[45]	AR38	MDA45
			SA_DQ[46]	AN39	MDA46
			SA_DQ[47]	AN40	MDA47
			SA_DQS[6]	AK38	DQSA6
			SA_DQS[6]	AK39	-DQSA6
			SA_DQ[48]	AL40	MDA48
			SA_DQ[49]	AL37	MDA49
			SA_DQ[50]	AJ38	MDA50
			SA_DQ[51]	AJ37	MDA51
			SA_DQ[52]	AL39	MDA52
			SA_DQ[53]	AL38	MDA53
			SA_DQ[54]	AJ39	MDA54
			SA_DQ[55]	AJ40	MDA55
			SA_DQS[7]	AF38	DQSA7
			SA_DQS[7]	AF39	-DQSA7
			SA_DQ[56]	AG40	MDA56
			SA_DQ[57]	AG37	MDA57
			SA_DQ[58]	AE38	MDA58
			SA_DQ[59]	AE37	MDA59
			SA_DQ[60]	AG39	MDA60
			SA_DQ[61]	AG38	MDA61
			SA_DQ[62]	AE39	MDA62
			SA_DQ[63]	AE40	MDA63

DDR_0

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LGA1155[10SC1-F01155-21R_10SC1-F01155-22R]

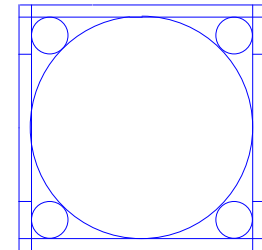
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MAAB2	AM19	SB_MAI[2]			
MAAB3	AK18	SB_MAI[3]			
MAAB4	AP19	SB_MAI[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MAI[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MAI[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MAI[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AY17	SB_MAI[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AY17	SB_MAI[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MAI[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MAI[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MAI[12]			
MAAB13	AR26	SB_MAI[13]	SB_DQS[1]	AM8	DQSB1
MAAB14	AY16	SB_MAI[14]	SB_DQS[1]	AL8	-DQSB1
MAAB15	AV16	SB_MAI[15]			
(8) -SWEB	AR25	SB_WE#	SB_DQ[8]	AL7	MDB8
(8) -SCASB	AK25	SB_CAS#	SB_DQ[9]	AM7	MDB9
(8) -SRASB	AP24	SB_RAS#	SB_DQ[10]	AM10	MDB10
(8) SBAB0	AP23	SB_BS[0]	SB_DQ[11]	AL6	MDB12
(8) SBAB1	AM26	SB_BS[1]	SB_DQ[12]	AL6	MDB13
(8) SBAB2	AW17	SB_BS[2]	SB_DQ[13]	AL9	MDB14
			SB_DQ[14]	AM9	MDB15
			SB_DQ[15]		
(8) -CSB0	AN25	SB_CS#	SB_DQS[2]	AR8	DQSB2
(8) -CSB1	AN26	SB_CS#	SB_DQS[2]	AP8	-DQSB2
(8) -CSB2	AL25	SB_CS#			
(8) -CSB3	AT26	SB_CS#			
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(8) CKEB1	AY15	SB_CKE[1]	SB_DQ[17]	AR7	MDB17
(8) CKEB2	AW15	SB_CKE[2]	SB_DQ[18]	AP10	MDB18
(8) CKEB3	AV15	SB_CKE[3]	SB_DQ[19]	AR10	MDB19
			SB_DQ[20]	AP6	MDB20
MODT_B0	AL26	SB_ODT[0]	SB_DQ[21]	AR6	MDB21
MODT_B1	AP26	SB_ODT[1]	SB_DQ[22]	AP9	MDB22
MODT_B2	AM26	SB_ODT[2]	SB_DQ[23]	AR9	MDB23
MODT_B3	AK26	SB_ODT[3]			
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(8) -DCLKB0	AL22	SB_CLK[0]	SB_DQS[3]	AN12	-DQSB3
(8) DCLKB1	AK20	SB_CLK[1]	SB_DQ[24]	AM12	MDB24
(8) -DCLKB1	AK20	SB_CLK[1]	SB_DQ[25]	AM13	MDB25
(8) DCLKB2	AL23	SB_CLK[2]	SB_DQ[26]	AR13	MDB26
(8) -DCLKB2	AL23	SB_CLK[2]	SB_DQ[27]	AP13	MDB27
(8) DCLKB3	AP21	SB_CLK[3]	SB_DQ[28]	AL12	MDB28
(8) -DCLKB3	AN21	SB_CLK[3]	SB_DQ[29]	AL13	MDB29
			SB_DQ[30]	AR12	MDB30
			SB_DQ[31]	AP12	MDB31
			SB_DQ[32]		
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			SB_DQ[32]	AR28	MDB32
			SB_DQ[33]	AR29	MDB33
			SB_DQ[34]	AL28	MDB34
			SB_DQ[35]	AL29	MDB35
			SB_DQ[36]	AP28	MDB36
			SB_DQ[37]	AP29	MDB37
			SB_DQ[38]	AM28	MDB38
			SB_DQ[39]	AM29	MDB39
			SB_DQS[5]	AP33	DQSB5
			SB_DQS[5]	AR33	-DQSB5
			SB_DQ[40]	AP32	MDB40
			SB_DQ[41]	AP31	MDB41
			SB_DQ[42]	AP35	MDB42
			SB_DQ[43]	AP34	MDB43
			SB_DQ[44]	AR32	MDB44
			SB_DQ[45]	AR31	MDB45
			SB_DQ[46]	AR35	MDB46
			SB_DQ[47]	AR34	MDB47
			SB_DQS[6]	AL33	DQSB6
			SB_DQS[6]	AM33	-DQSB6
			SB_DQ[48]	AM32	MDB48
			SB_DQ[49]	AM31	MDB49
			SB_DQ[50]	AL35	MDB50
			SB_DQ[51]	AL32	MDB51
			SB_DQ[52]	AM34	MDB52
			SB_DQ[53]	AL31	MDB53
			SB_DQ[54]	AM35	MDB54
			SB_DQ[55]	AL34	MDB55
			SB_DQS[7]	AG35	DQSB7
			SB_DQS[7]	AG34	-DQSB7
			SB_DQ[56]	AH35	MDB56
			SB_DQ[57]	AH34	MDB57
			SB_DQ[58]	AE34	MDB58
			SB_DQ[59]	AE35	MDB59
			SB_DQ[60]	AJ35	MDB60
			SB_DQ[61]	AJ34	MDB61
			SB_DQ[62]	AF33	MDB62
			SB_DQ[63]	AF35	MDB63

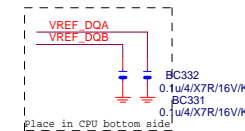
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LGA1155[10SC1-F01155-21R_10SC1-F01155-22R]

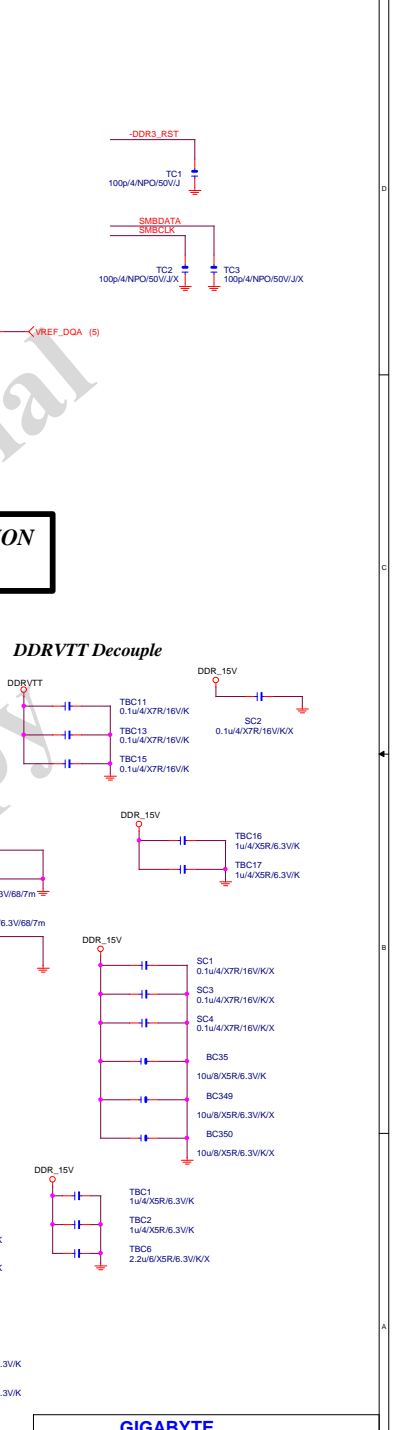
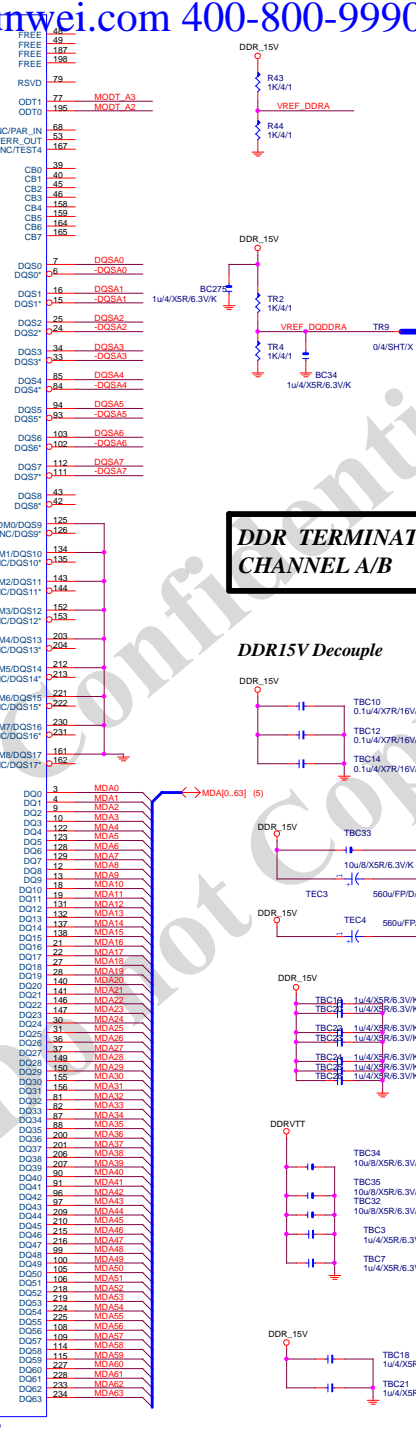
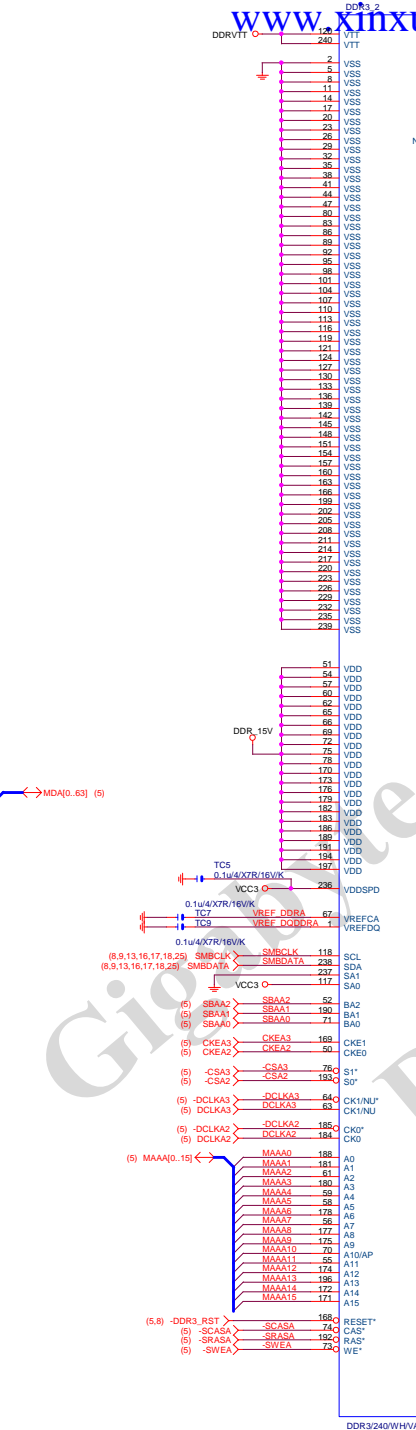
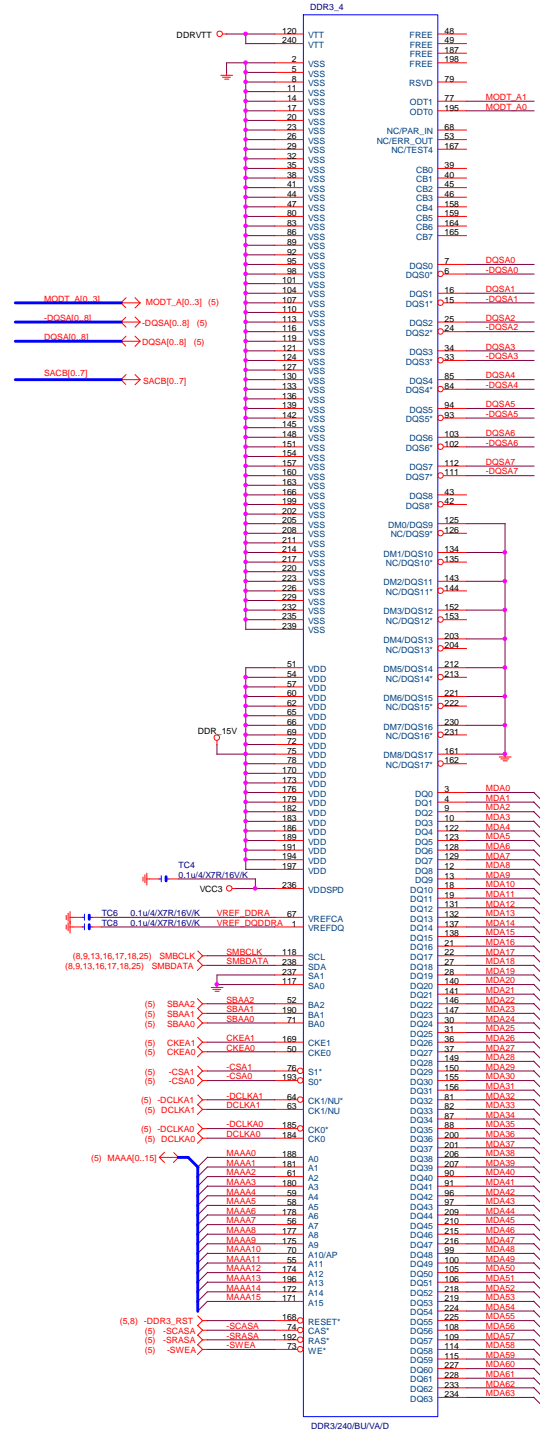
CPU
ILM_BP/1156/CSP

Need check the new CPU ME



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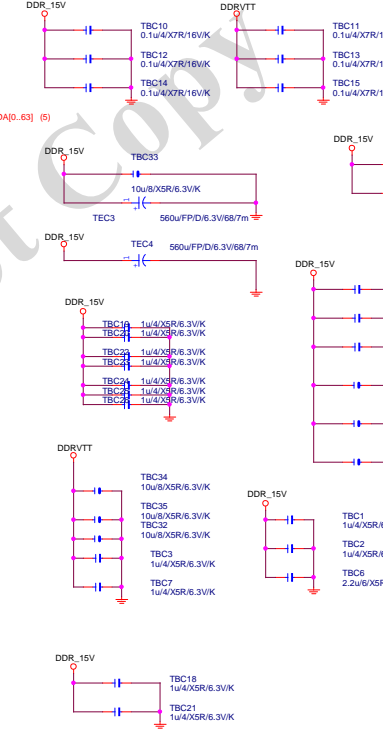
Title			CPU LGA1156-B		
Size			ST4265-BI		
Date:			Monday, December 03, 2012		
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DDR TERMINATION CHANNEL A/B

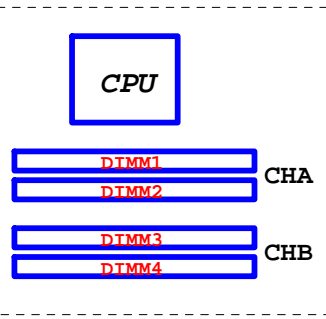
DDR15V Decouple

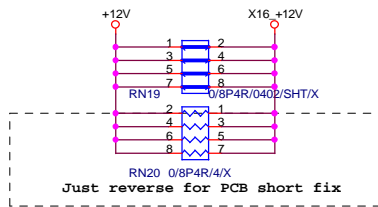
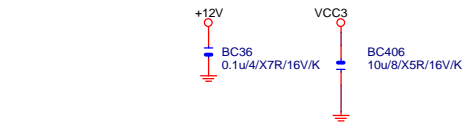
DDRVTT Decouple



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Title		DDR3 CHANNEL A	
Size		Custom	
Date		Rev 1.2	





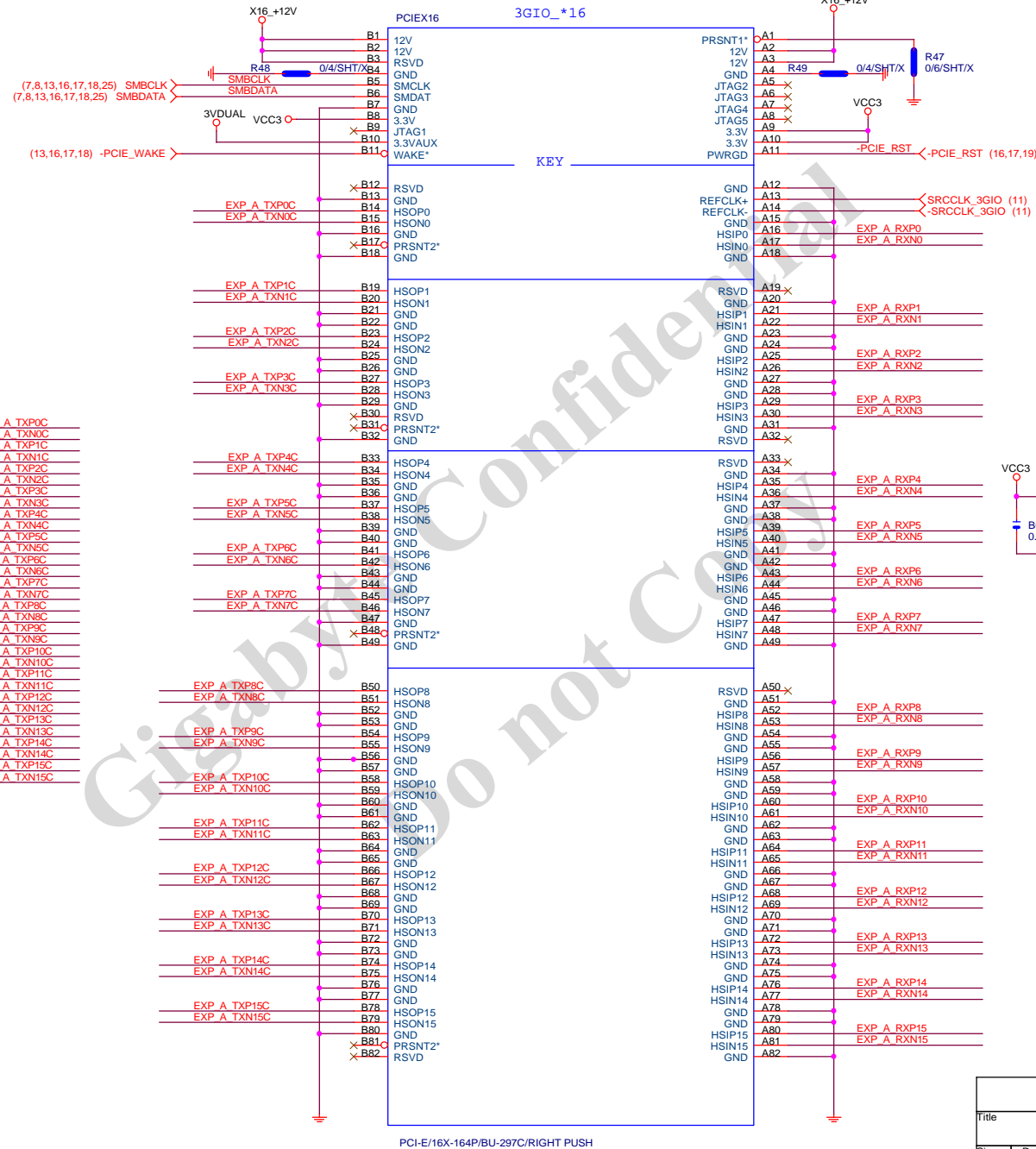
EXP A RXP0_15I >>> EXP_A_RXP0_15I (4)

EXP A RXN0_15I >>> EXP_A_RXN0_15I (4)

EXP A TXP0_15I >>> EXP_A_TXP0_15I (4)

EXP A TXN0_15I >>> EXP_A_TXN0_15I (4)

EXP A TXP0	C2	0.22u4/X5R/6.3V/K	EXP A TXP0C
EXP A TXN0	C3	0.22u4/X5R/6.3V/K	EXP A TXN0C
EXP A TXP1	C4	0.22u4/X5R/6.3V/K	EXP A TXP1C
EXP A TXN1	C5	0.22u4/X5R/6.3V/K	EXP A TXN1C
EXP A TXP2	C6	0.22u4/X5R/6.3V/K	EXP A TXP2C
EXP A TXN2	C7	0.22u4/X5R/6.3V/K	EXP A TXN2C
EXP A TXP3	C8	0.22u4/X5R/6.3V/K	EXP A TXP3C
EXP A TXN3	C9	0.22u4/X5R/6.3V/K	EXP A TXN3C
EXP A TXP4	C10	0.22u4/X5R/6.3V/K	EXP A TXP4C
EXP A TXN4	C11	0.22u4/X5R/6.3V/K	EXP A TXN4C
EXP A TXP5	C12	0.22u4/X5R/6.3V/K	EXP A TXP5C
EXP A TXN5	C13	0.22u4/X5R/6.3V/K	EXP A TXN5C
EXP A TXP6	C14	0.22u4/X5R/6.3V/K	EXP A TXP6C
EXP A TXN6	C15	0.22u4/X5R/6.3V/K	EXP A TXN6C
EXP A TXP7	C16	0.22u4/X5R/6.3V/K	EXP A TXP7C
EXP A TXN7	C17	0.22u4/X5R/6.3V/K	EXP A TXN7C
EXP A TXP8	C18	0.22u4/X5R/6.3V/K	EXP A TXP8C
EXP A TXN8	C19	0.22u4/X5R/6.3V/K	EXP A TXN8C
EXP A TXP9	C20	0.22u4/X5R/6.3V/K	EXP A TXP9C
EXP A TXN9	C21	0.22u4/X5R/6.3V/K	EXP A TXN9C
EXP A TXP10	C22	0.22u4/X5R/6.3V/K	EXP A TXP10C
EXP A TXN10	C23	0.22u4/X5R/6.3V/K	EXP A TXN10C
EXP A TXP11	C24	0.22u4/X5R/6.3V/K	EXP A TXP11C
EXP A TXN11	C25	0.22u4/X5R/6.3V/K	EXP A TXN11C
EXP A TXP12	C26	0.22u4/X5R/6.3V/K	EXP A TXP12C
EXP A TXN12	C27	0.22u4/X5R/6.3V/K	EXP A TXN12C
EXP A TXP13	C28	0.22u4/X5R/6.3V/K	EXP A TXP13C
EXP A TXN13	C29	0.22u4/X5R/6.3V/K	EXP A TXN13C
EXP A TXP14	C30	0.22u4/X5R/6.3V/K	EXP A TXP14C
EXP A TXN14	C31	0.22u4/X5R/6.3V/K	EXP A TXN14C
EXP A TXP15	C32	0.22u4/X5R/6.3V/K	EXP A TXP15C
EXP A TXN15	C33	0.22u4/X5R/6.3V/K	EXP A TXN15C

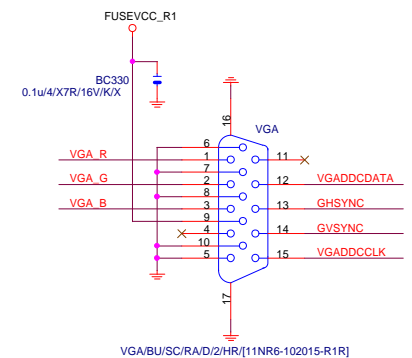
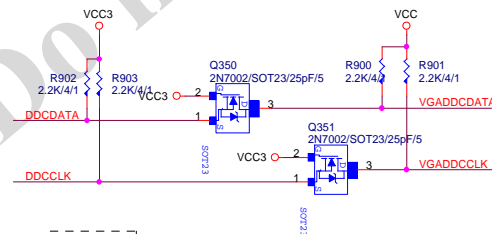
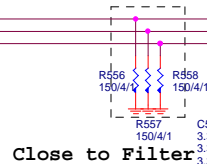
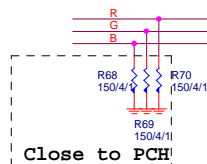
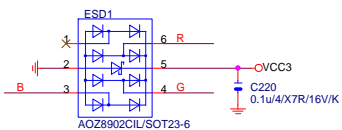
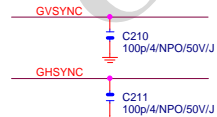
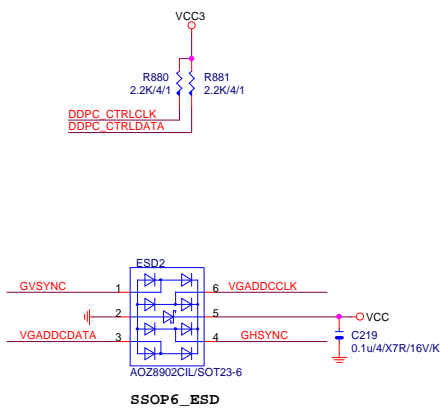
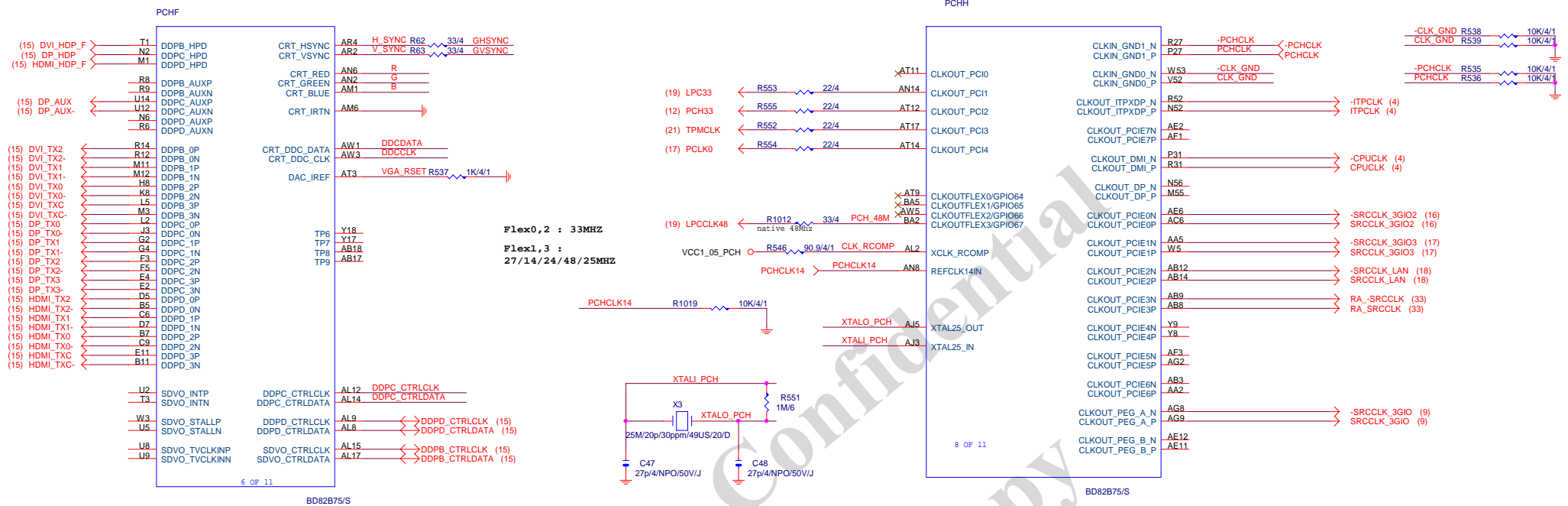


PCI-E/16X-164P/BU-297C/RIGHT PUSH

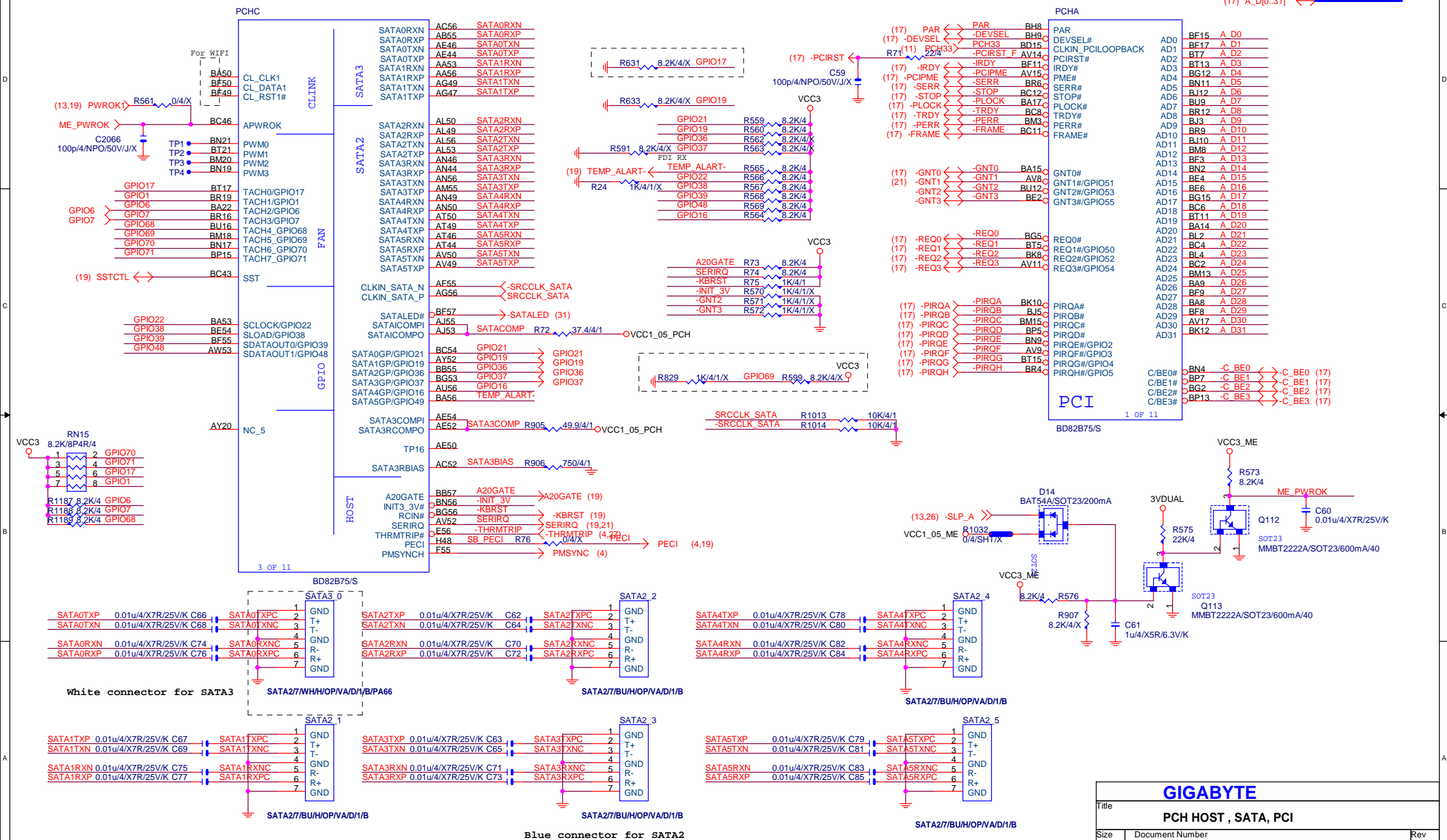
GIGABYTE

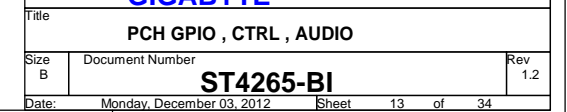
Title			
PCI EXPRESS * 16			
Size			
Custom			
Document Number			
ST4265-BI			
Rev			
1.2			
Date:			
Monday, December 03, 2012			
Sheet			
9 of 34			

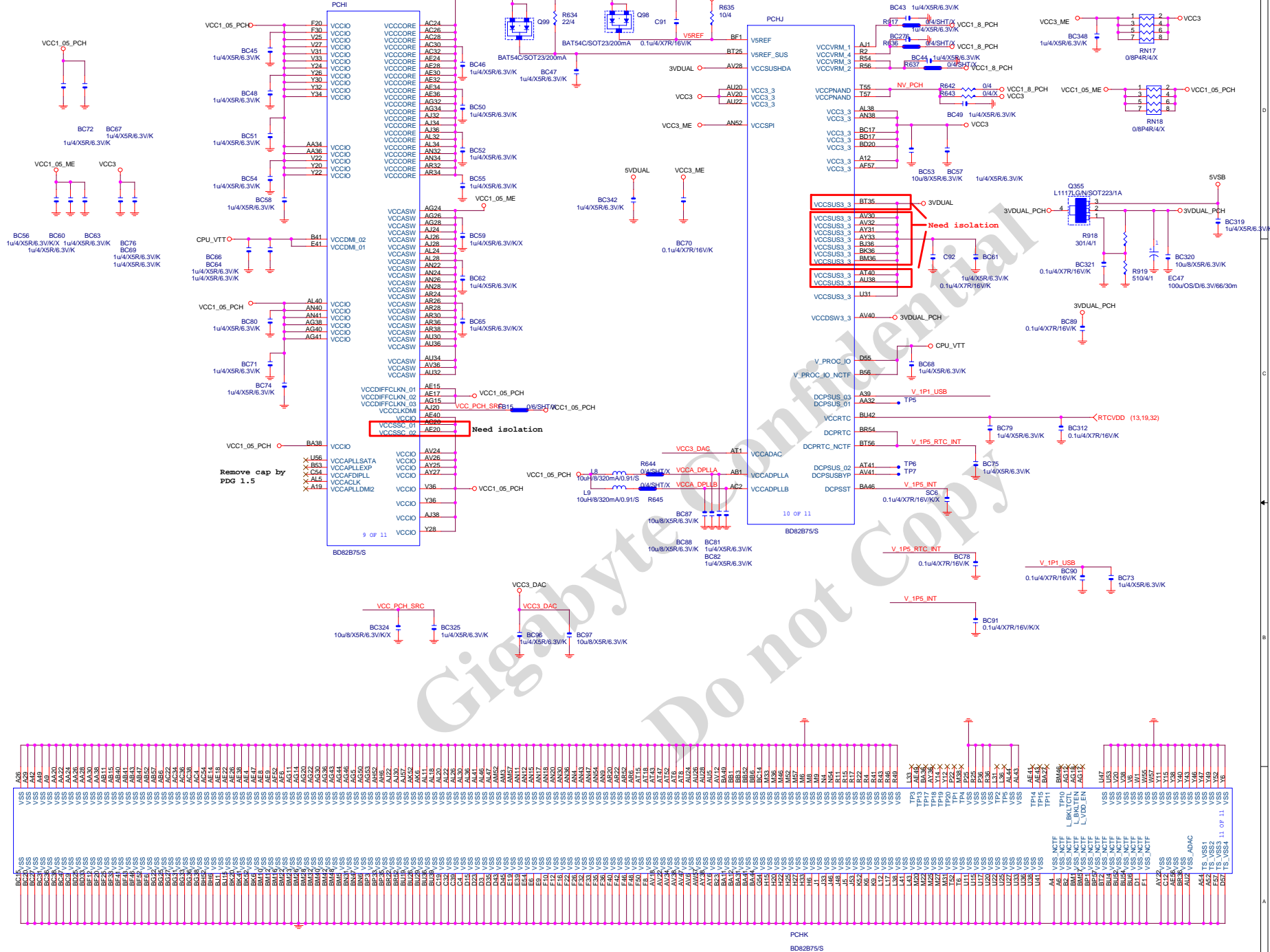


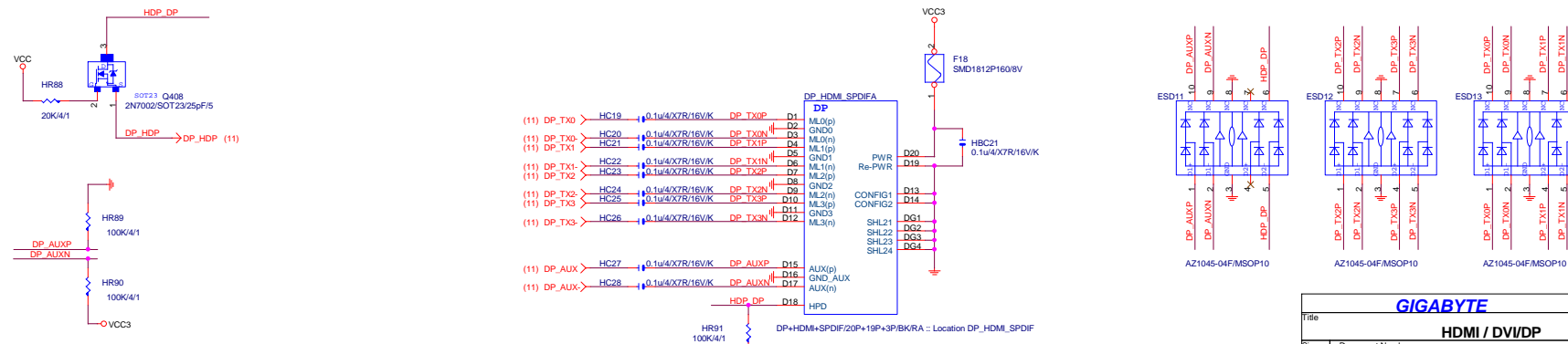
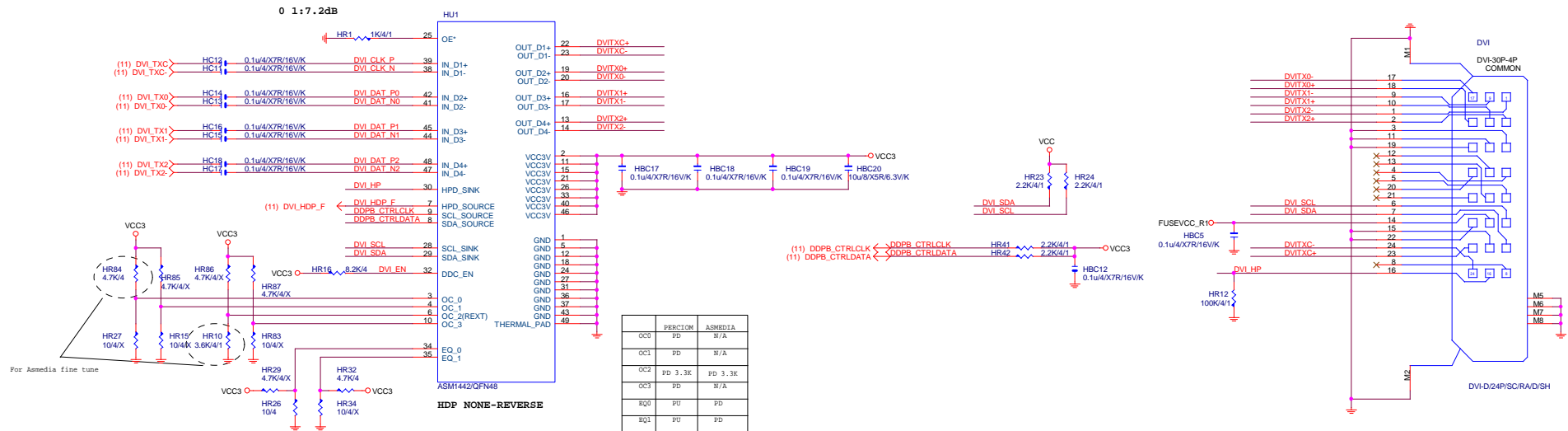
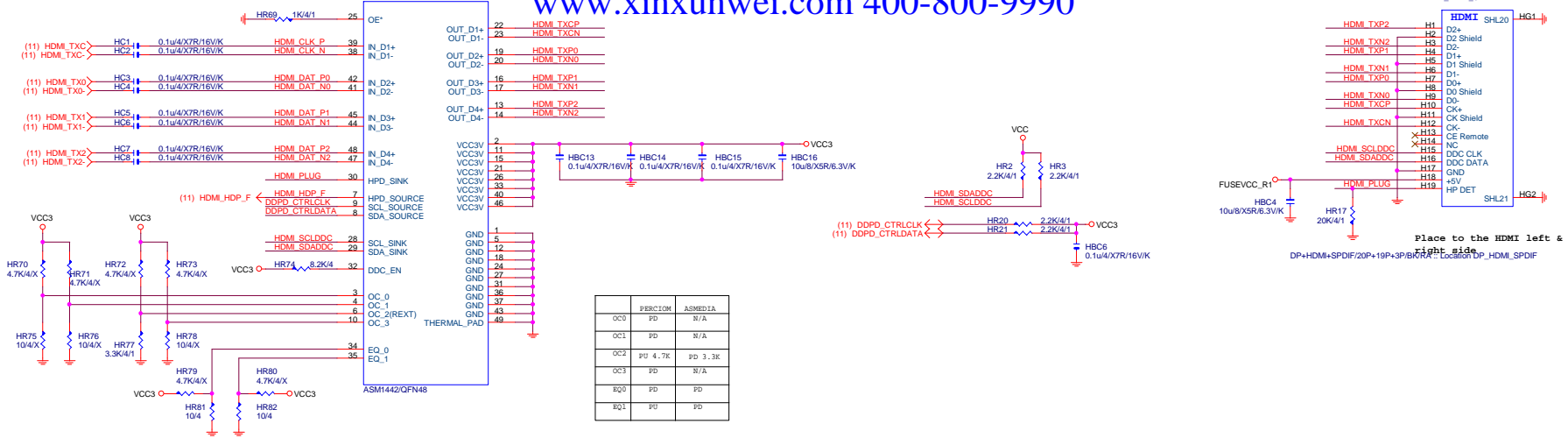


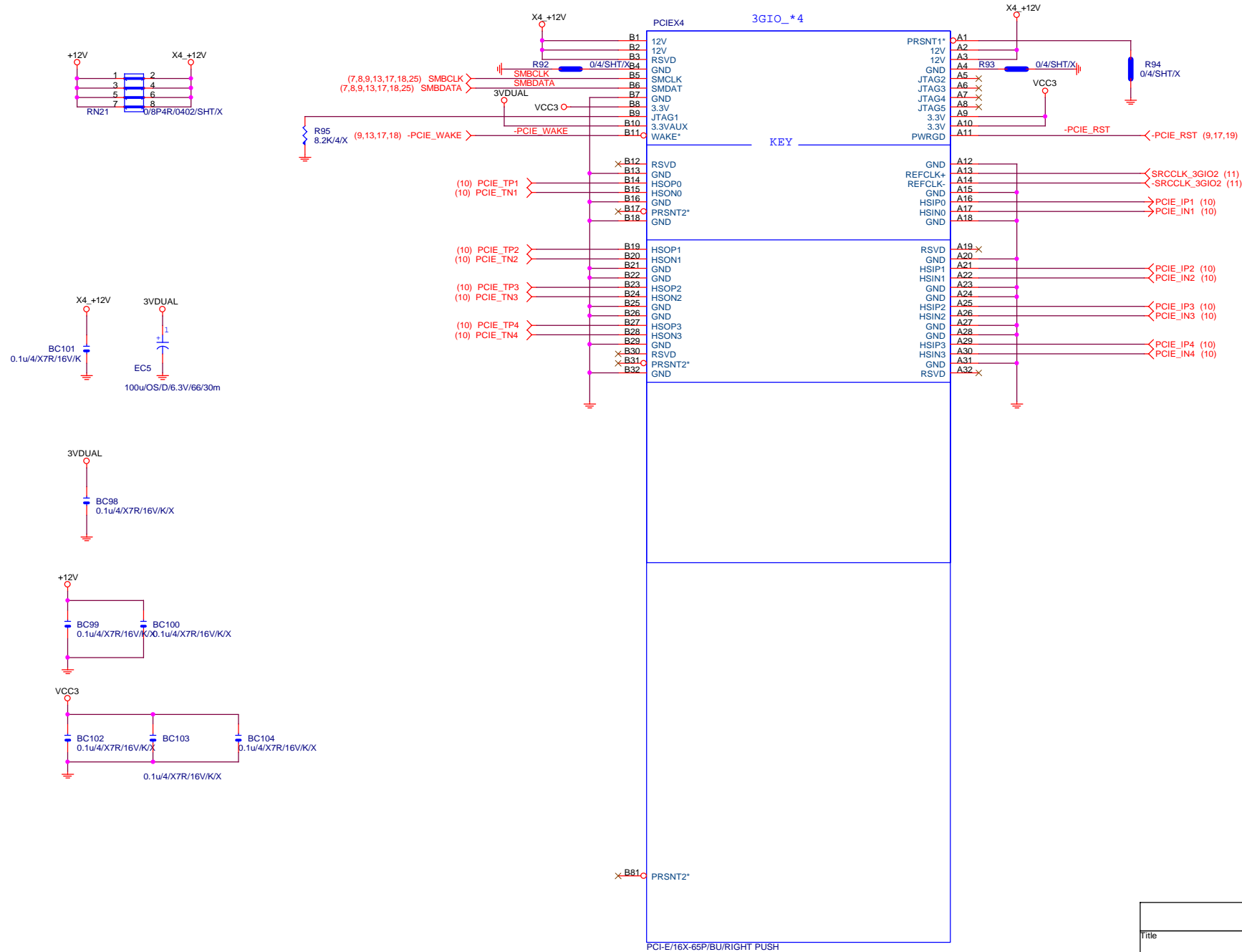
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PCH DISPLAY ,CLK BUFFER			
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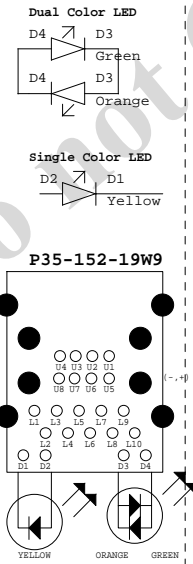
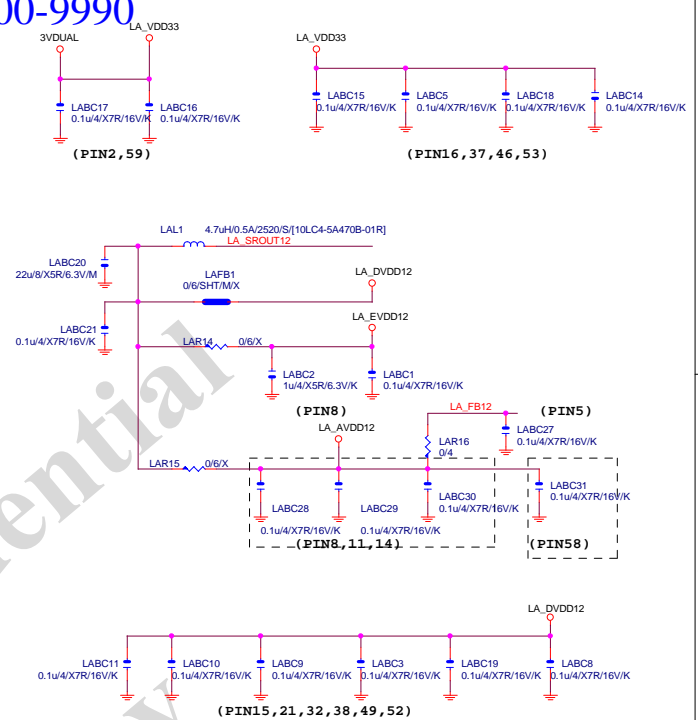


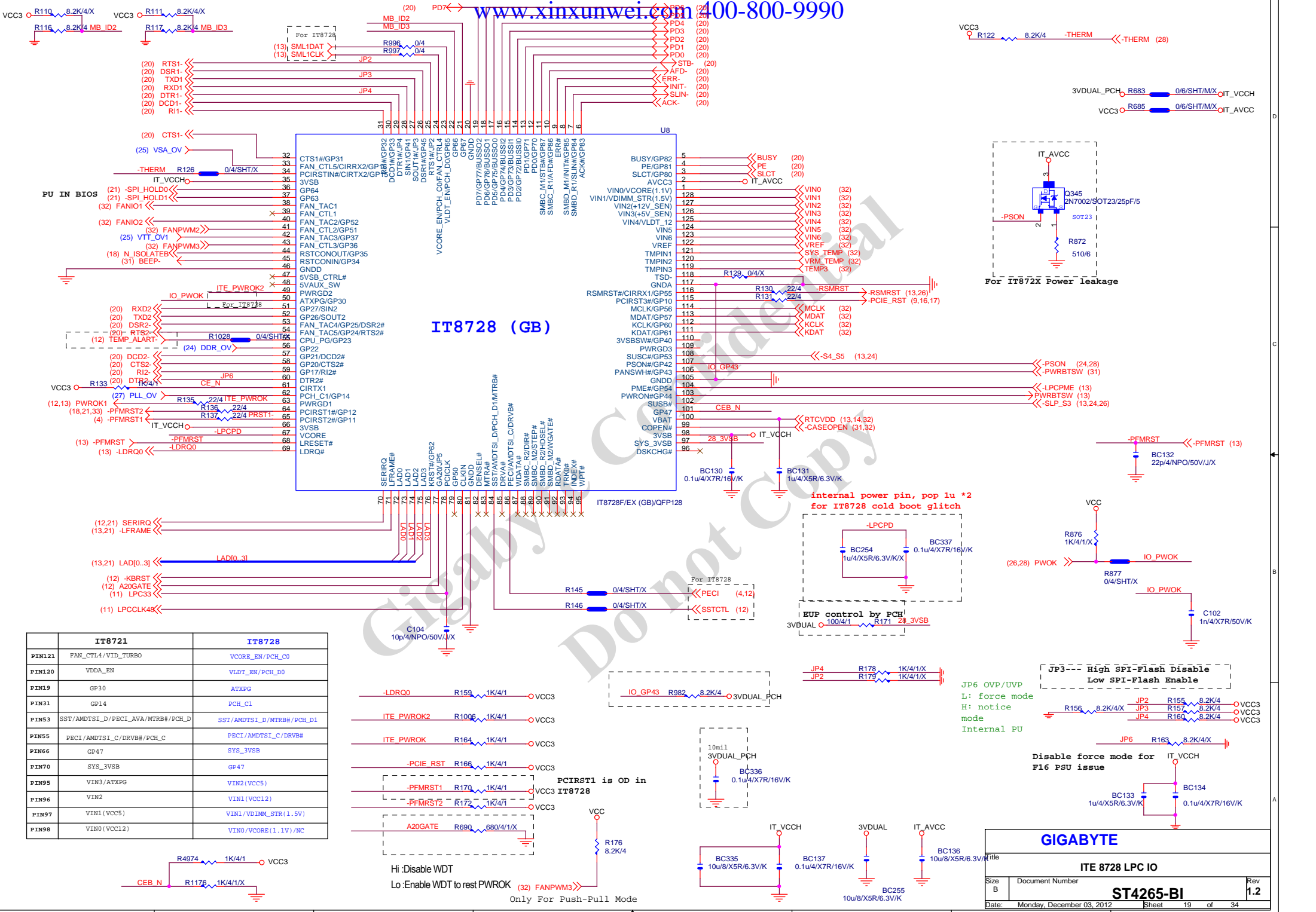
GIGABYTE

Title		
PCI EXPRESS X 4 PORT		
Size	Document Number	Rev
Custom	ST4265-BI	1.2
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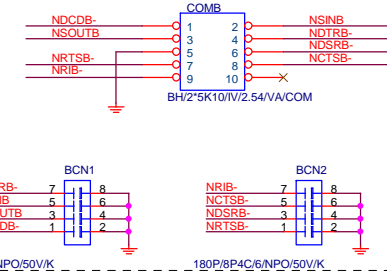
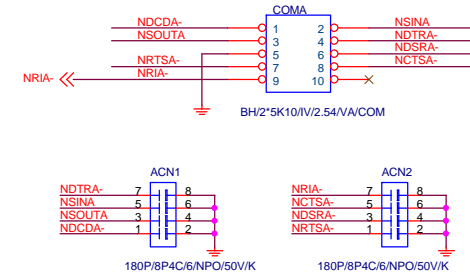


Title			
PCIEX1,PCI SLOT			
Size Custom	Document Number	Rev	
	ST4265-BI	1.2	
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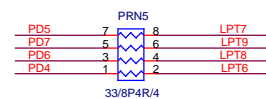




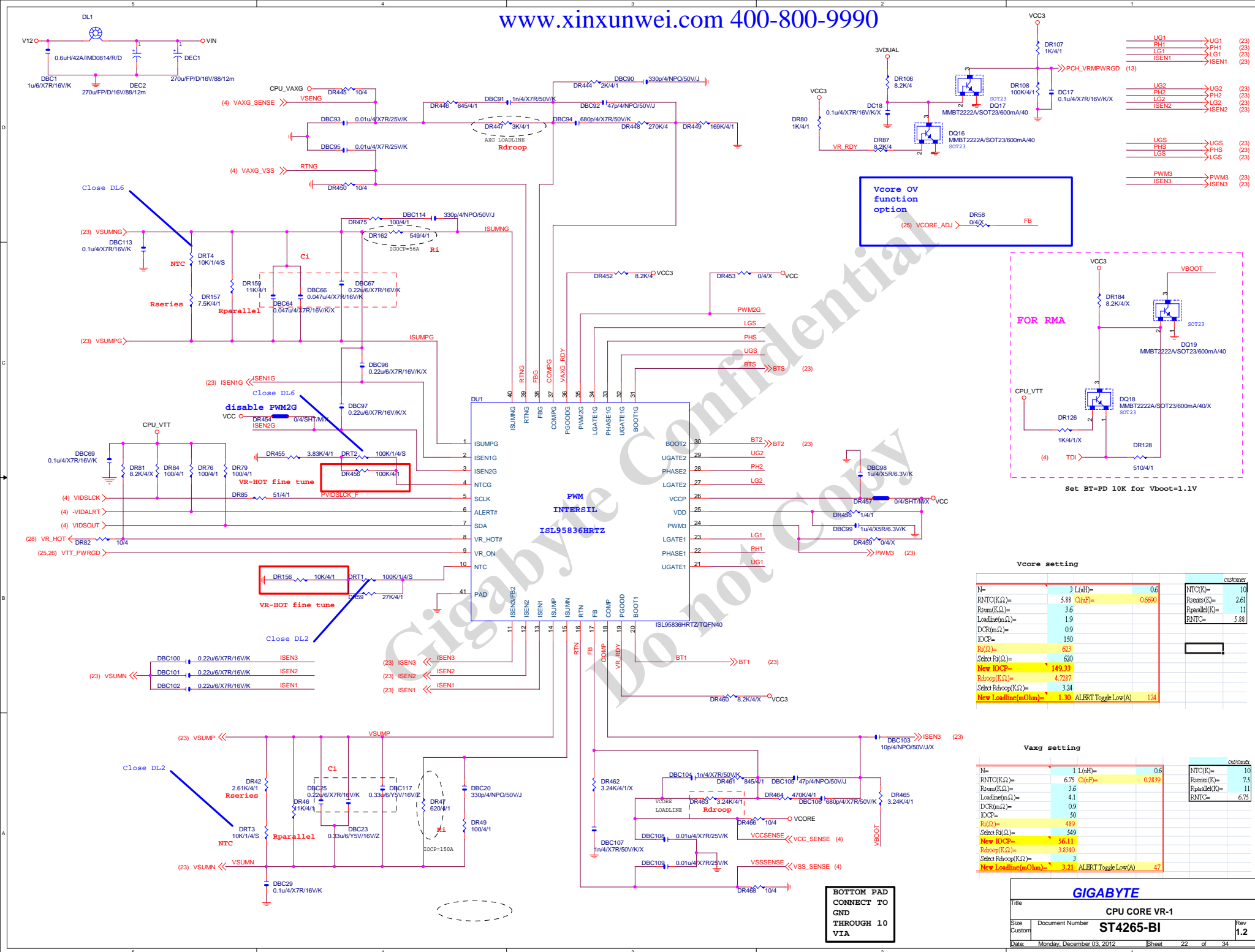
COMB



LPT PORT



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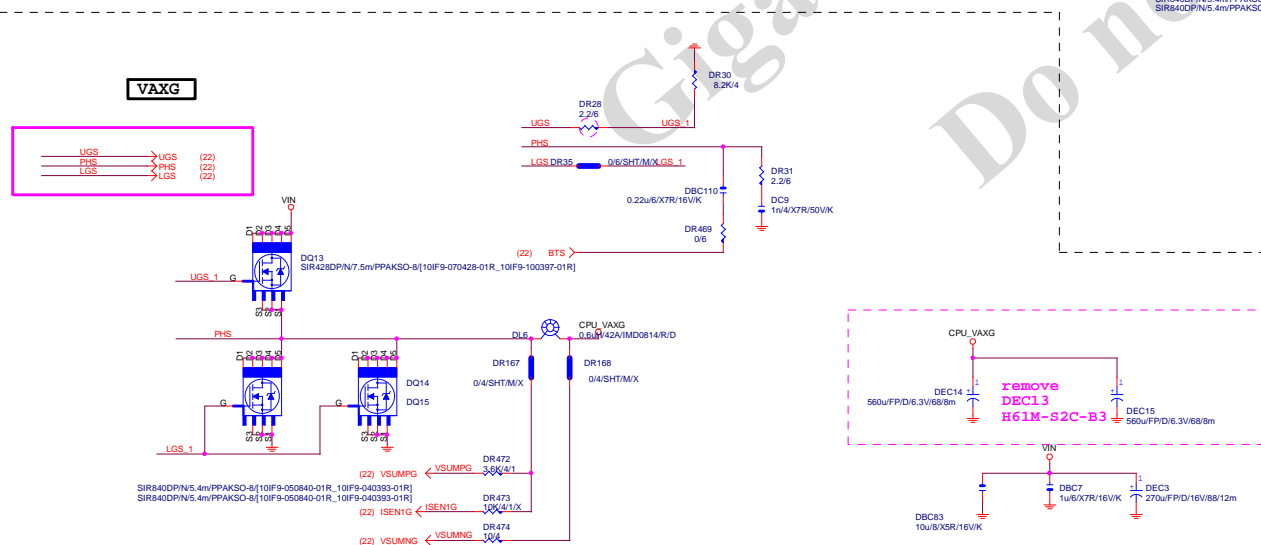
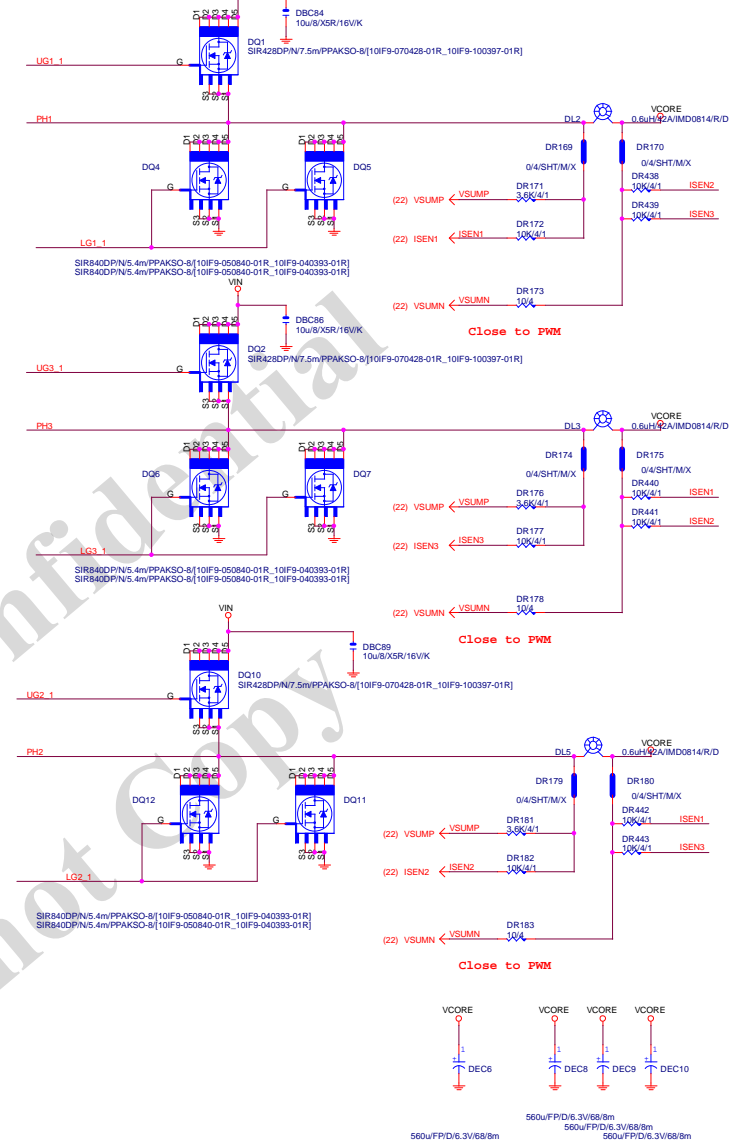
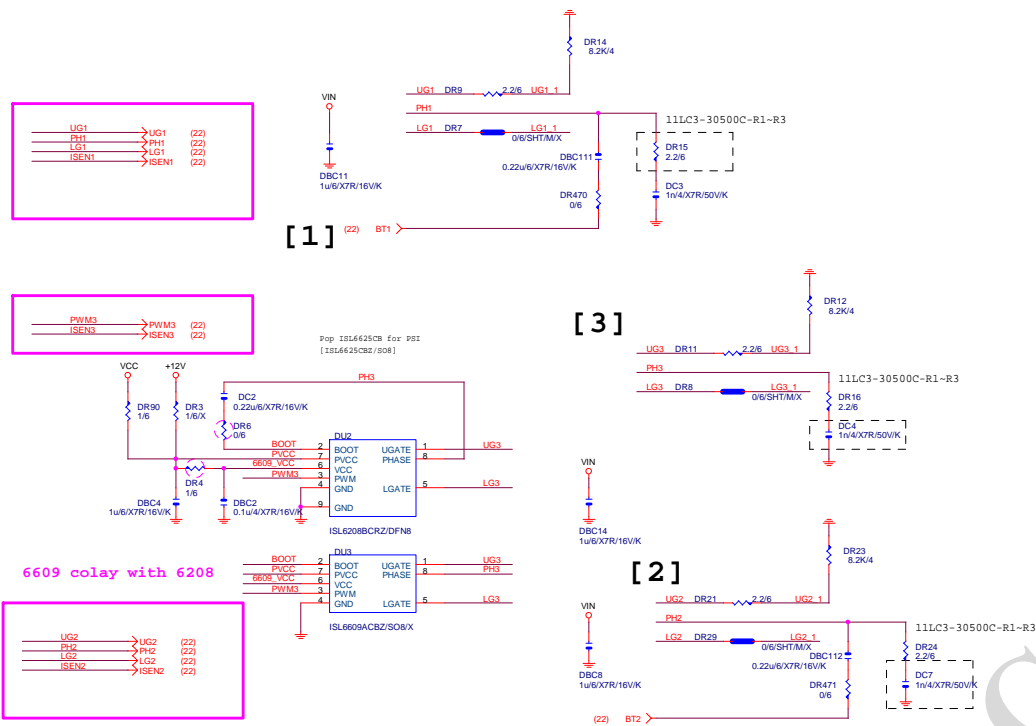
Voore setting			
N=	3	L(nH)=	0.6
RNTC(K Ω)=	5.88	Q(nH)=	0.6690
Routn(K Ω)=	3.6		
Loadline(m Ω)=	1.9		
DOR(m Ω)=	0.9		
IOCP=	150		
Ri(Ω)=	623		
Select Ri(Ω)=	620		
New IOCP=	149.33		
Rdroopn(K Ω)=	4.7287		
Select Rdroopn(K Ω)=	3.24		
New Loadline(mOhm)=	1.30	ALERT Toggle Low(A)	124

Vaux setting			cost
N=	1	L(0f)	0.6
RNTC(K _Ω)	6.75	G(nP)	0.2839
Fsum(K _Ω)		3.6	
Loaeline(m _Ω)		4.1	
DCR(m _Ω)		0.9	
IOCP=		50	
Fit(Ω)		489	
Select Fit(Ω)		549	
New IOCP=		56.11	
Radrop(T _Ω)		3.8340	
Select Radrop(K _Ω)		3	
New Loeline(mOhm)=	3.21	ALERT Toggle Low(A)	47

GIGABYTE

Title	CPU CORE VR-
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Custom				
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DDR_15V

OCP :
 $Rocset = (Iocp * Lgate, rdson) / Iocset$
 $Iocset = 10uA$

VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
 IRMS=11.45A

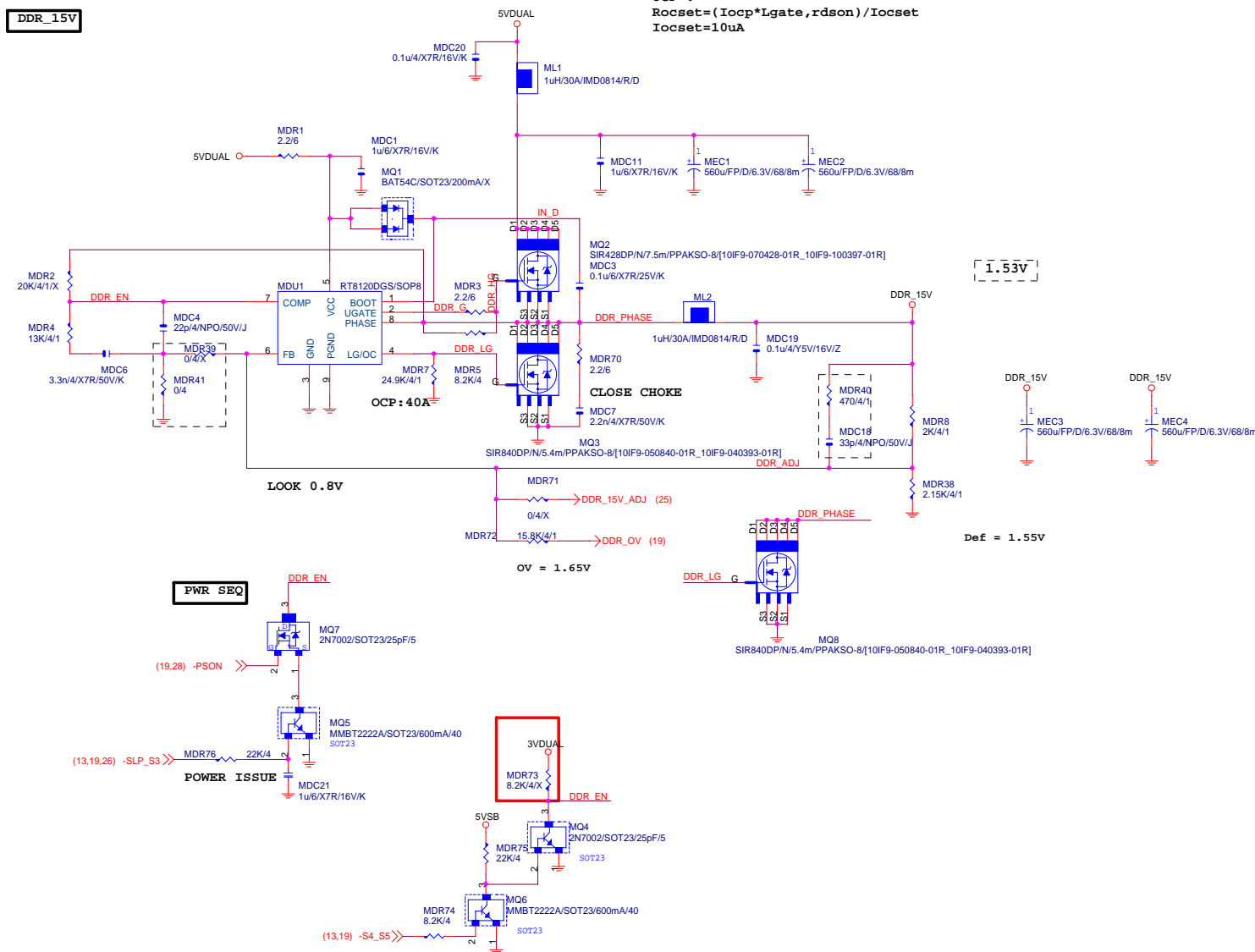
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
 Coefficient=1.7(85°C), 1(105°C)

VIN Ripple current=4.7X1.7=7.99A(85°C)

-->故固态电容须 $2X7.99=15.98 > 11.45A$

OCP : $Ipeak = (2 * Iocset * Rocset) / Rdson$
 typ Iocset=20uA , Rocset=4.7k

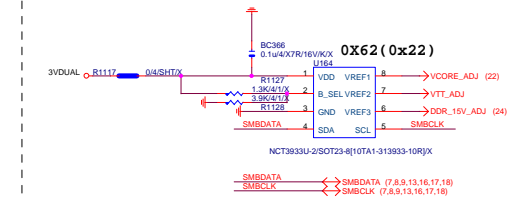
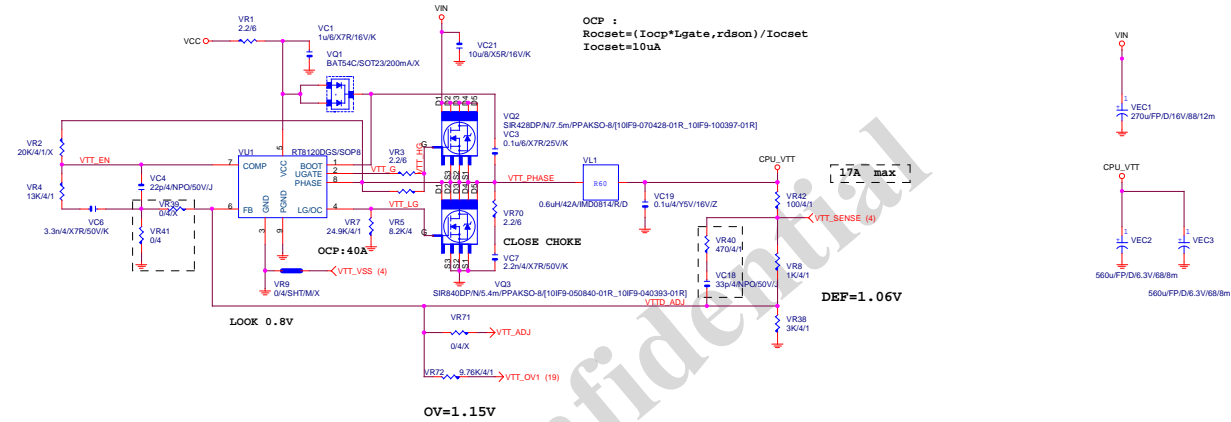
OCP : $53.71A = (2 * 20uA * 4.7k) / (7m / 7m)$



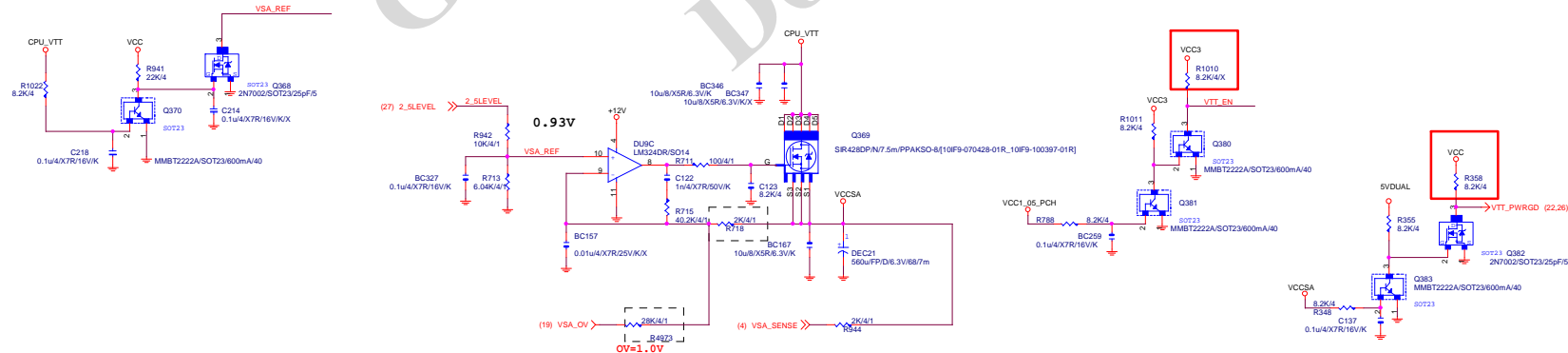
GIGABYTE

Title		
DDR POWER		
Size	Document Number	Rev
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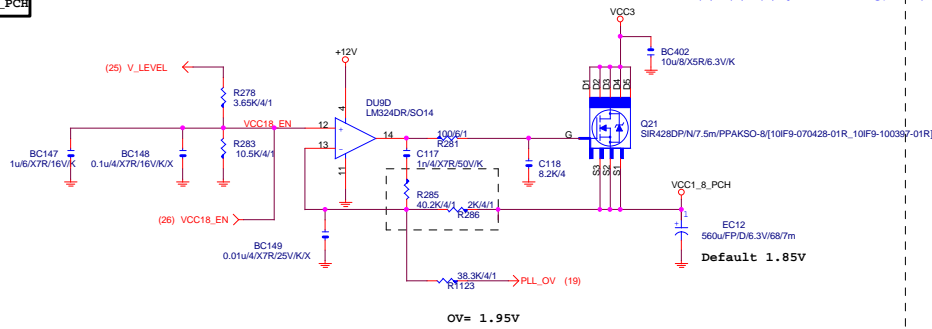
CPU_VTT



VCC_SA



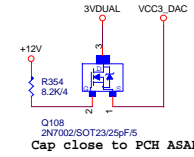
VCC1_8_PCH



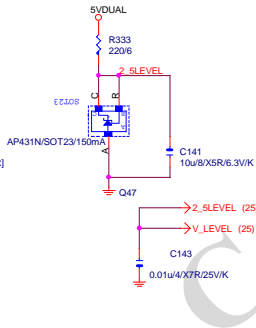
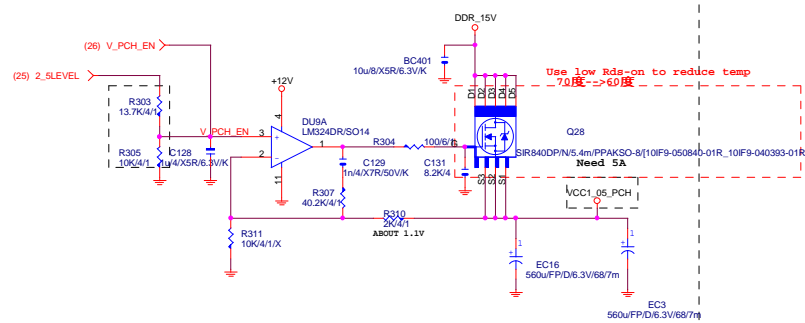
VCC3_DAC

(3.3V/70mA+360uA)

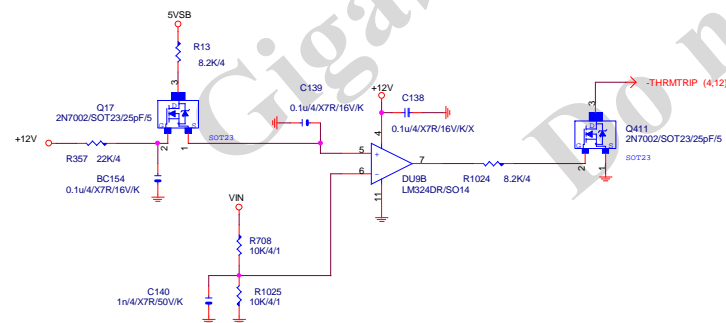
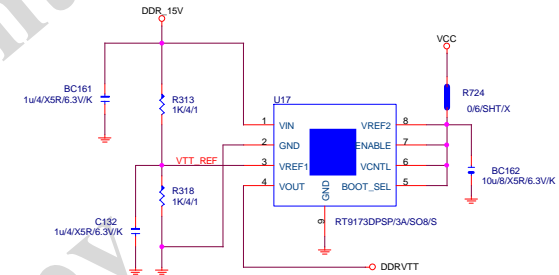
CLOSE PCH (注意震盪水波紋)



VCC1_05_PCH

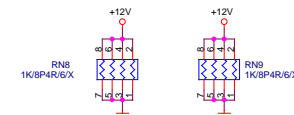
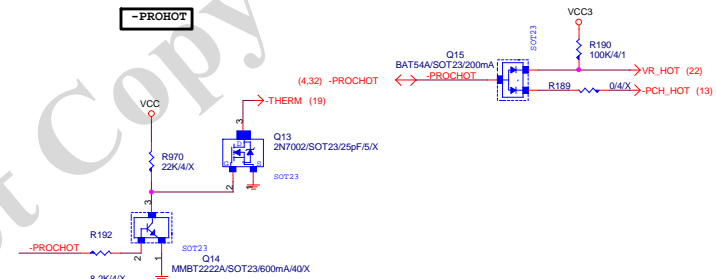
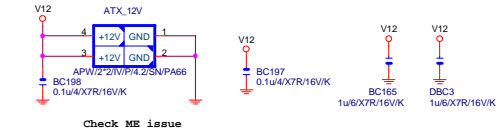


DDRVTT

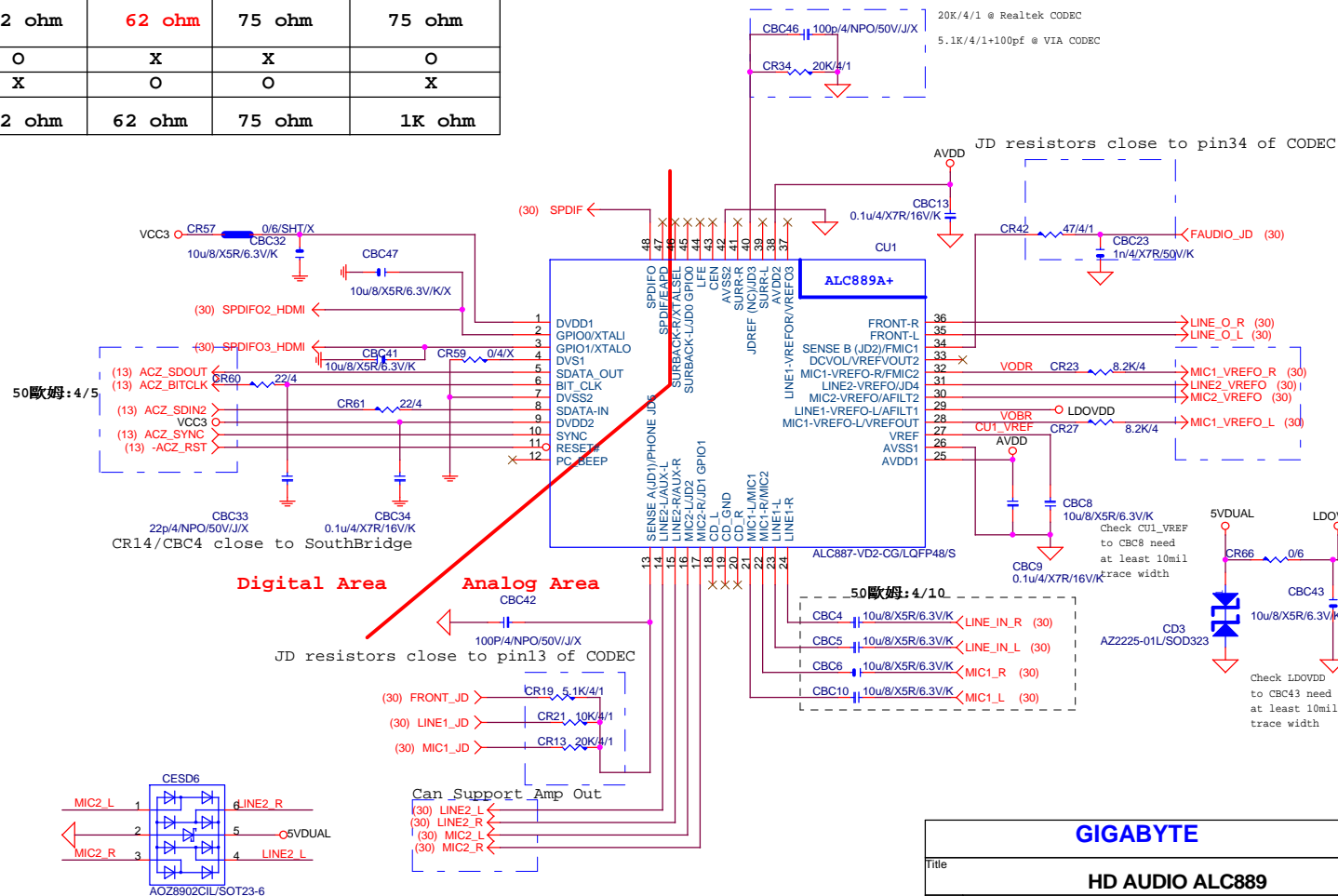


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File	DISCRETE POWER II	Rev
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	ALC662	ALC887-VD2	ALC889	VT1708S	VT1708SCE
CR59	X	X	O	O	X
CR	X	X	X	X	0.1u/4
CBC41	O	O	X	X	O
CR42/CBC23	47ohm+1nF	47ohm+1nF	47ohm+1nF	22ohm+100P	22ohm+100P
CR63	X	O	O	O	O
CR56	O	X	X	X	X
CBC4/CBC5	22uF/X5R	22uF/X5R	22uF/X5R	22uF/X5R	22uF/X5R
CR19	5.11K/4/1	5.11K/4/1	5.11K/4/1	5.1K/4/1	5.1K/4/1
CR34	20K/4/1	20K/4/1	20K/4/1	5.1K/4/1	20K/4/1
CBC42/CBC46	N/A	N/A	N/A	100P/4	100P/4
CR14/CR33/CR28/CR45/ CR41/CR46/CR2/CR4/ CR35/CR39	22K/4	22K/4	22K/4	10K/4	10K/4
CR32/CR15/CR51/CR25 /CR52/CR40/CR3/CR1/ CR9/CR6/CR36/CR38	62 ohm	62 ohm	62 ohm	75 ohm	75 ohm
CR66/CD3/CBC43	O	O	X	X	O
CD1/CD2/CQ4/CQ5/CBC36	X	X	O	O	X
CR12/CR17/CR22/CR26	62 ohm	62 ohm	62 ohm	75 ohm	1K ohm

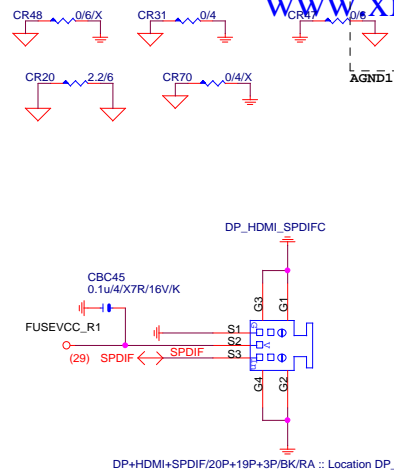
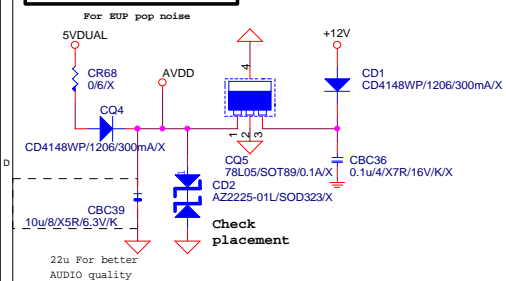


0/6/X For AGND/GND
moat under Codec
Body

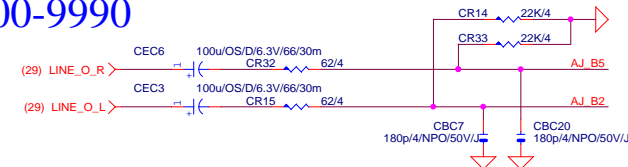
GIGABYTE

Title			
HD AUDIO ALC889			
Size	Document Number		
Custom	ST4265-BI		
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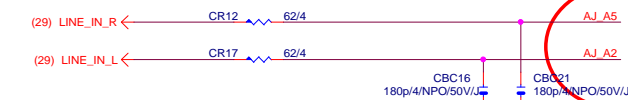
CODEC POWER/EMI PAD



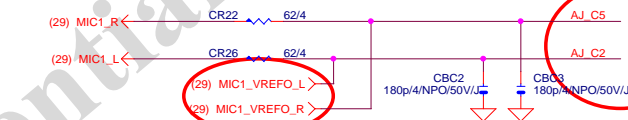
LINE-OUT



LINE-IN



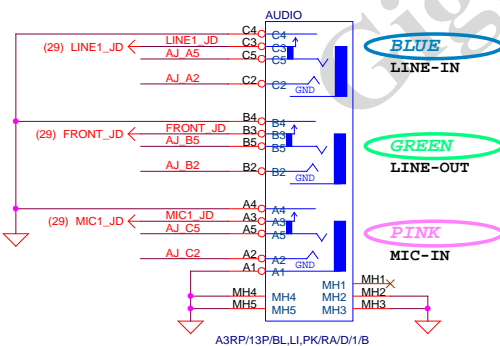
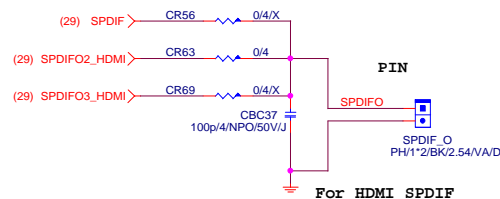
MIC-IN



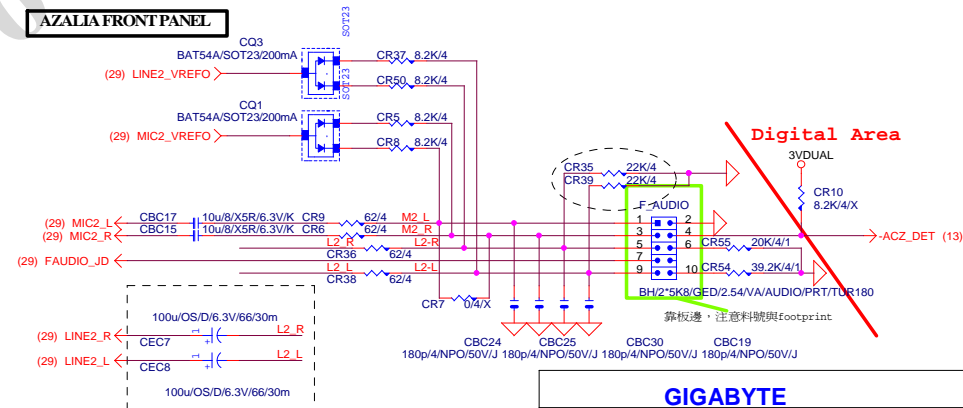
SURROUND

CEN/LFE

SURRBACK

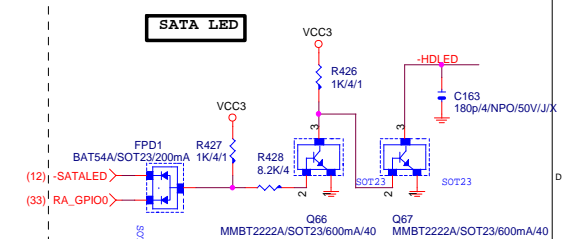


AZALIA FRONT PANEL

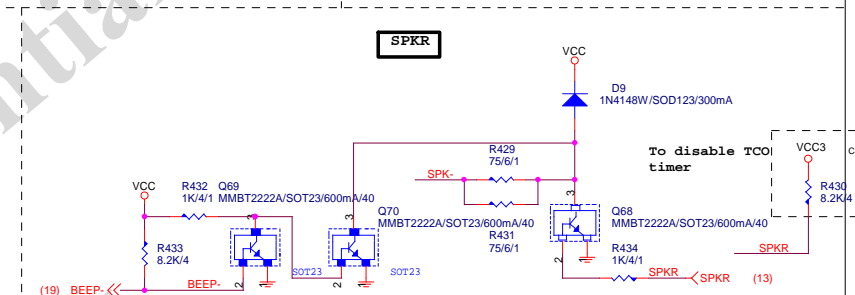


GIGABYTE

Title		
AUDIO JACK		
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Close to connector



INTEL FRONT PANEL

ESD9

AOZ8902CIL/SOT23-6

BC220
0.01u/4/X7R/25V/K/X

BC222
0.01u/4/X7R/25V/K

R438
330/6

R437
330/6

R439
8.2K/4

Q71
MMBT2222A/SOT23/600mA/40

R443
100/4/1

C164
0.01u/4/X7R/25V/K/X

R442
33/4

FP_PANEL

HD+ MSG/PD+ 1

HD- MSG/PD- 2

GND 3

RESET 4

CI- 5

CI+ 6

SP+ 7

NC 8

PWR+ 9

PWR- 10

PWR- 11

SPK- 12

MPD+ 13

MPD+ 14

MPD+ 15

MPD+ 16

MPD+ 17

MPD+ 18

MPD+ 19

MPD+ 20

MPD+ 21

MPD+ 22

MPD+ 23

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MPD+ 279

MPD+ 280

MPD+ 281

MPD+ 282

MPD+ 283

MPD+ 284

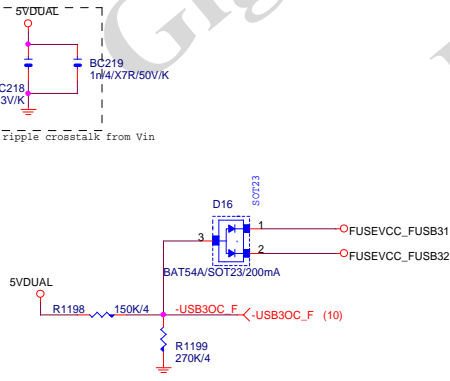
MPD+ 285

MPD+ 286

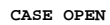
MPD+ 287

MPD+ 288

MP



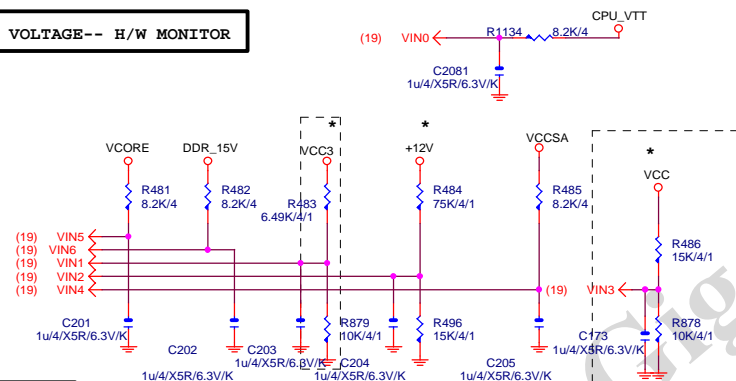
CPU SMART FAN



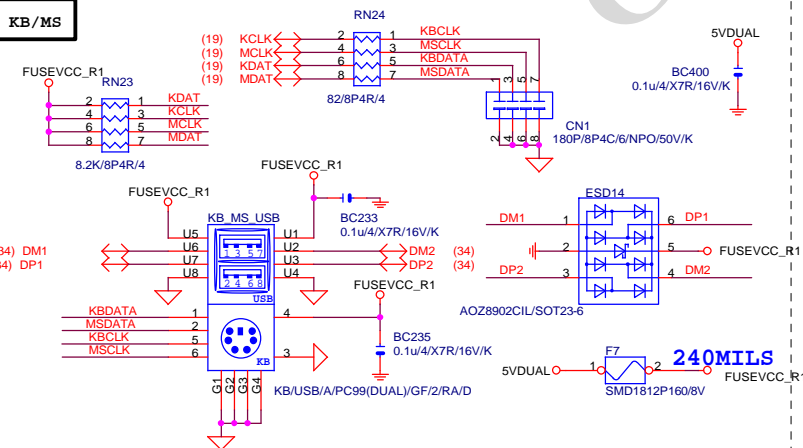
(13,14,19) RTCVDD ←  → -CASEOPEN (19,31)

Case Open Circuits

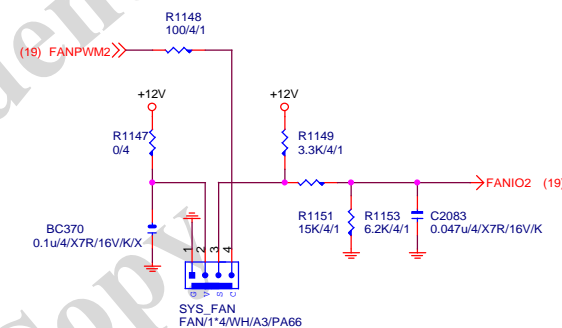
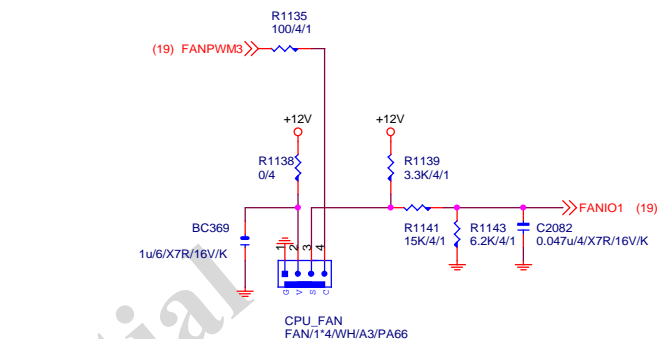
VOLTAGE-- H/W MONITOR



KB/MS

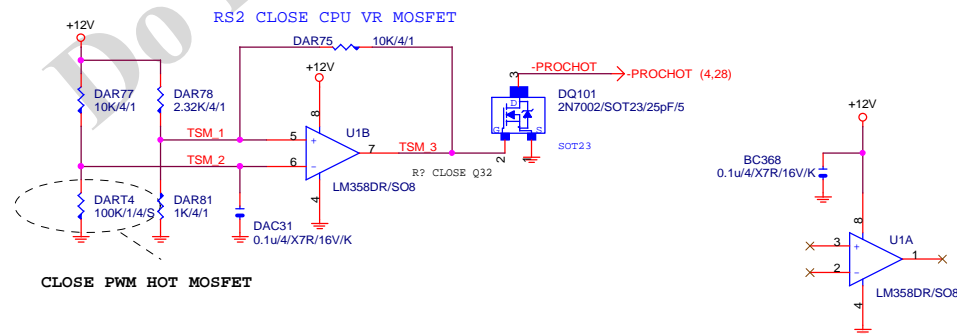


SYS FAN



-PROHOT

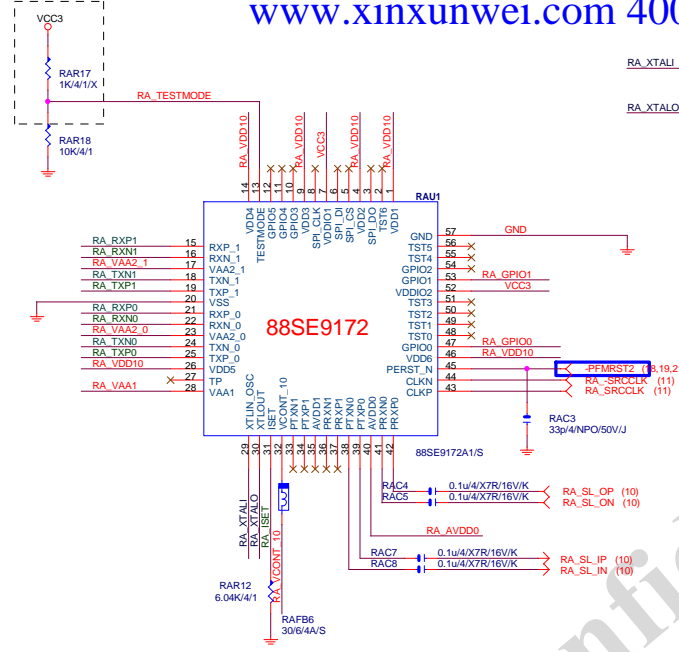
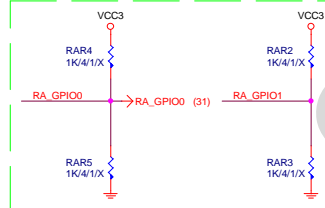
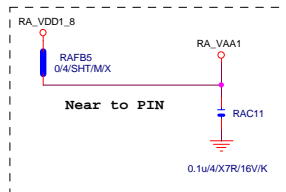
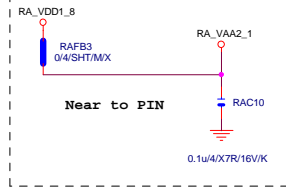
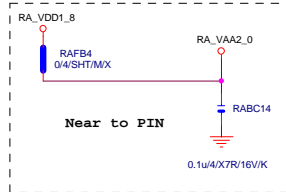
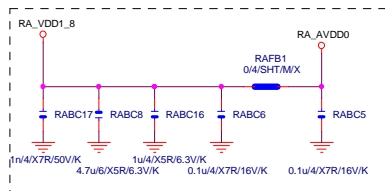
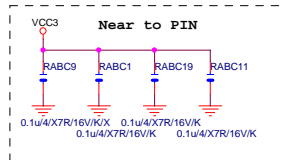
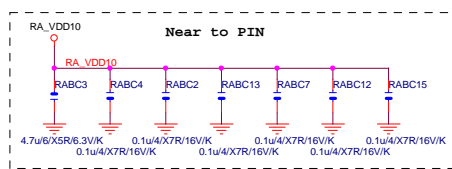
deasserted at 116 degree



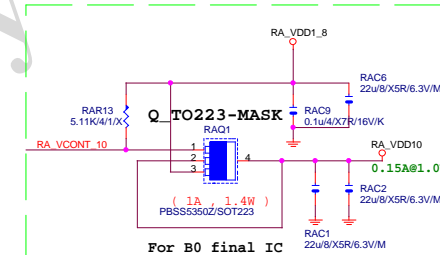
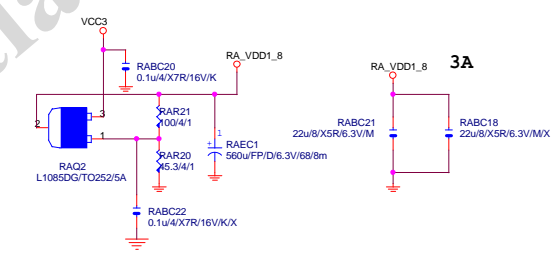
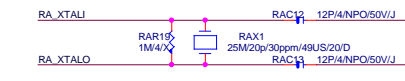
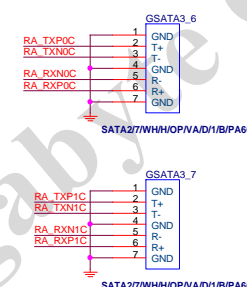
CLOSE PWM HOT MOSFET

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Title				HWM,KB/MS, FAN CTRL			
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RA_TXP0	0.01u/4/X7R/25V/K	RAC21	RA_TXP0C
RA_TXN0	0.01u/4/X7R/25V/K	RAC20	RA_TXN0C
RA_RXN0	0.01u/4/X7R/25V/K	RAC19	RA_RXN0C
RA_RXP0	0.01u/4/X7R/25V/K	RAC18	RA_RXP0C
RA_TXP1	0.01u/4/X7R/25V/K	RAC16	RA_TXP1C
RA_TXN1	0.01u/4/X7R/25V/K	RAC17	RA_TXN1C
RA_RXN1	0.01u/4/X7R/25V/K	RAC15	RA_RXN1C
RA_RXP1	0.01u/4/X7R/25V/K	RAC14	RA_RXP1C

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